Operating Systems

Lecture 27: I/O devices

Nipun Batra Nov 9, 2018

Motivation

What good is a computer without any I/O devices?

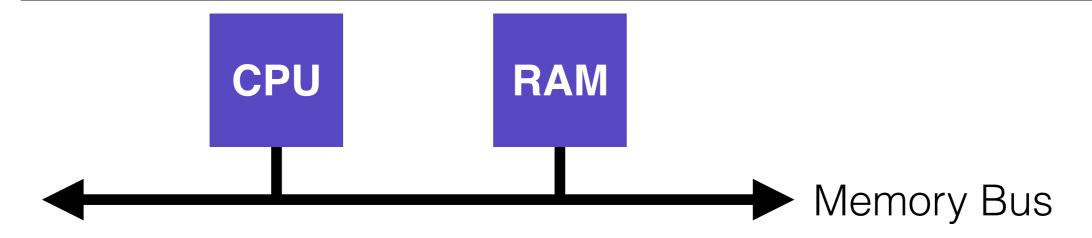
- keyboard, display, disks

We want:

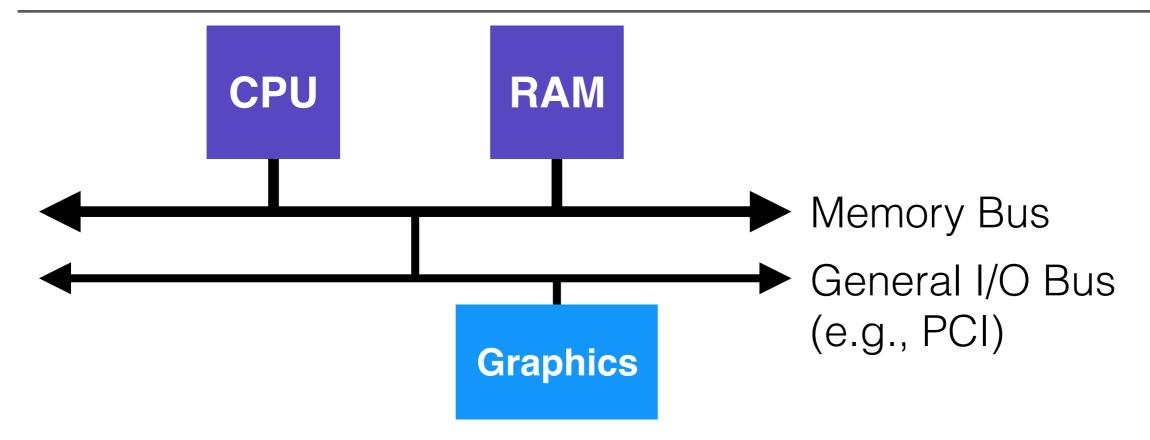
- H/W that will let us plug in different devices
- OS that can interact with different combinations

Largely a communication problem...

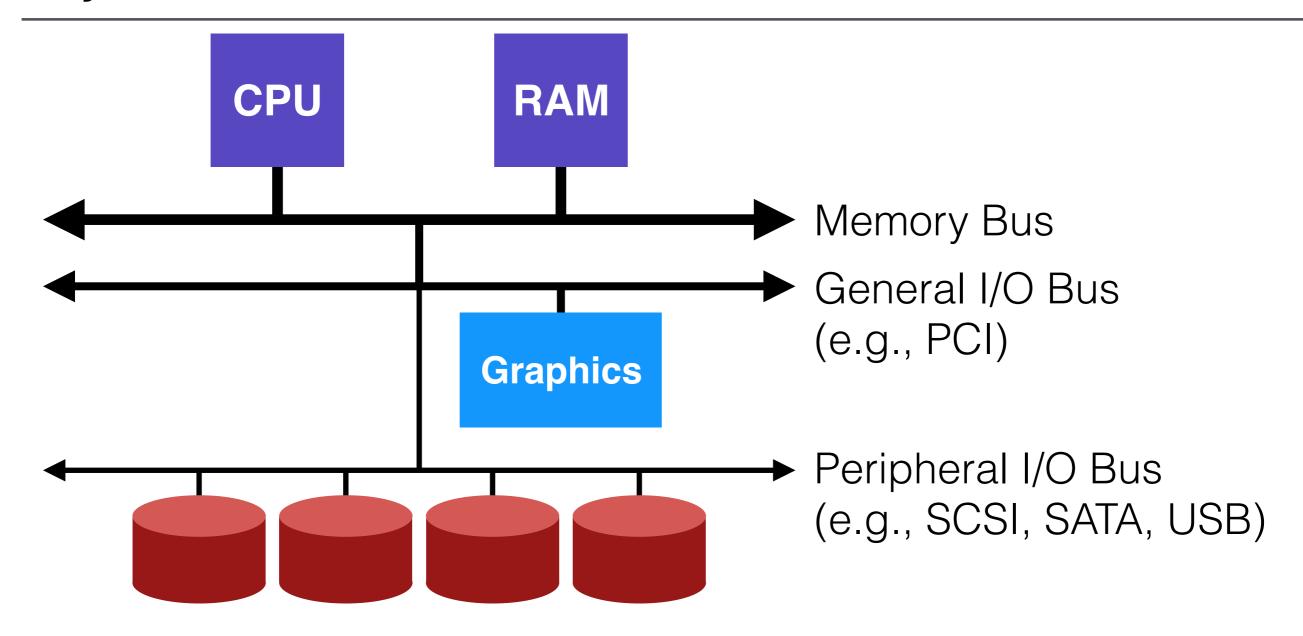
System Architecture



System Architecture



System Architecture



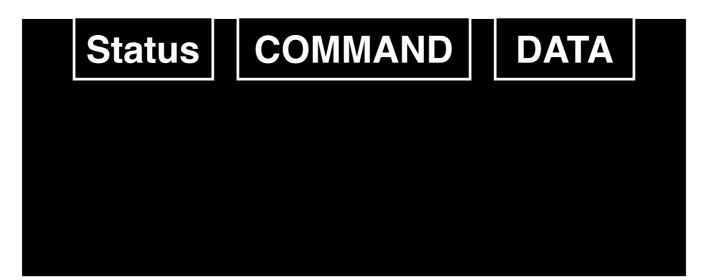
Why use hierarchical buses?

Device Registers:



OS reads/writes to these

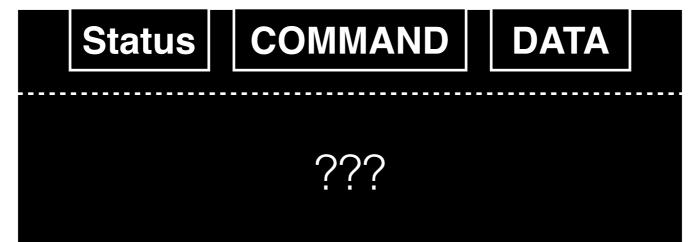
Device Registers:



OS reads/writes to these

Device Registers:

Hidden Internals:



OS reads/writes to these

Device Registers:

Hidden Internals:

Status COMMAND DATA

Microcontroller (CPU+RAM)
Extra RAM
Other special-purpose chips

Example Protocol

```
while (STATUS == BUSY)
  ; // spin
Write data to DATA register
Write command to COMMAND register
while (STATUS == BUSY)
  ; // spin
```

CPU:

Disk:

```
while (STATUS == BUSY)  // 1
;
Write data to DATA register  // 2
Write command to COMMAND register  // 3
while (STATUS == BUSY)  // 4
;
```

CPU: A

Disk: C

CPU: A wants to do I/O

Disk: C

```
CPU: A
```

Disk: C

```
while (STATUS == BUSY)  // 1
;
Write data to DATA register  // 2
Write command to COMMAND register  // 3
while (STATUS == BUSY)  // 4
:
```

```
CPU:
Disk:
   while (STATUS == BUSY)
                                       // 1
   Write data to DATA register
   Write command to COMMAND register // 3
   while (STATUS == BUSY)
                                       // 4
```

```
Example
  CPU:
  Disk:
     while (STATUS == BUSY)
                                         // 1
     Write data to DATA register
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                         // 4
```

```
Example
  CPU:
  Disk:
     while (STATUS == BUSY)
                                        // 1
     Write data to DATA register
                                       // 2
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                        // 4
```

```
Example
  CPU:
  Disk:
                      A
     while (STATUS == BUSY)
                                         // 1
     Write data to DATA register
                                       // 2
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                         // 4
```

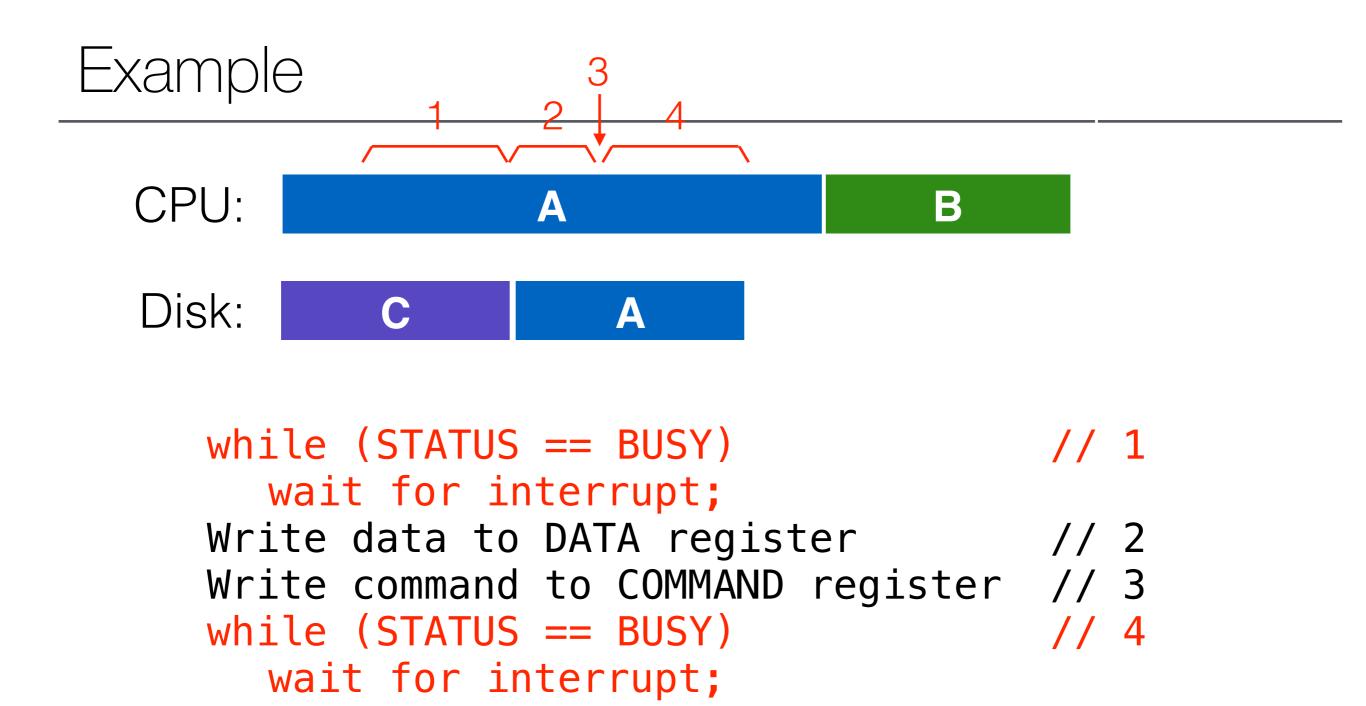
```
Example
  CPU:
                                    B
  Disk:
                      A
     while (STATUS == BUSY)
                                         // 1
     Write data to DATA register
                                        // 2
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                         // 4
```

```
Example
  CPU:
                                    B
  Disk:
                       A
     while (STATUS == BUSY)
                                         // 1
     Write data to DATA register
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                         // 4
```

How to avoid spinning?

```
Example
  CPU:
                                    B
  Disk:
                       A
     while (STATUS == BUSY)
                                         // 1
     Write data to DATA register
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
```

How to avoid spinning? Interrupts!



```
Example
                     3,4
  CPU:
              B
  Disk:
                       A
     while (STATUS == BUSY)
                                          // 1
        wait for interrupt;
     Write data to DATA register
                                         // 2
     Write command to COMMAND register // 3
     while (STATUS == BUSY)
                                          // 4
```

wait for interrupt;

Discuss: are interrupts ever worse?

Discuss: are interrupts ever worse?

Interrupts can sometimes lead to livelock

- e.g., flood of network packets

• Discuss: are interrupts ever worse?

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets
- Techniques:

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets
- Techniques:
 - hybrid approach

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets
- Techniques:
 - hybrid approach
 - Poll for a while, then wait for interrupts

- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets
- Techniques:
 - hybrid approach
 - Poll for a while, then wait for interrupts
 - interrupt coalescing

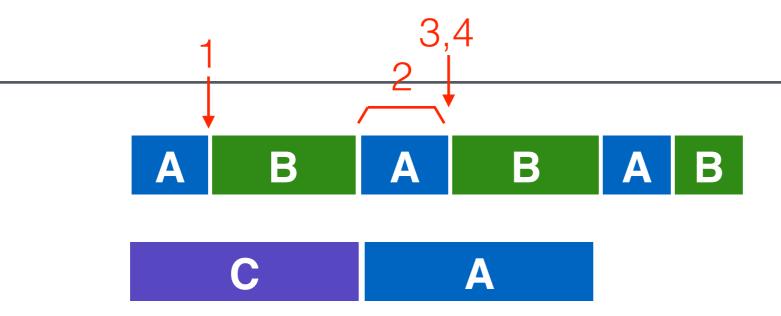
- Discuss: are interrupts ever worse?
- Interrupts can sometimes lead to livelock
 - e.g., flood of network packets
- Techniques:
 - hybrid approach
 - Poll for a while, then wait for interrupts
 - interrupt coalescing
 - Coalesce or combine the delivery of multiple interrupts

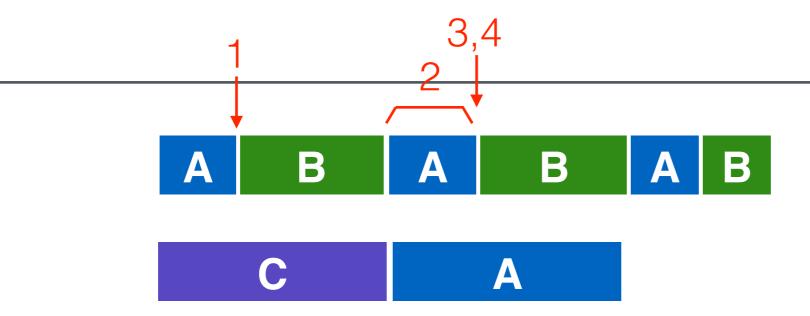
Protocol Variants

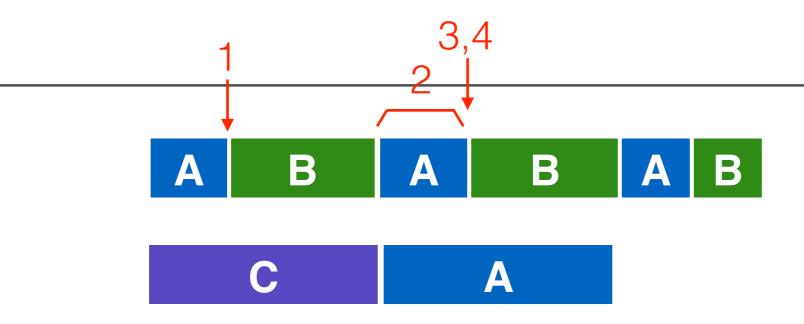
Status checks: polling vs. interrupts

Data: PIO vs. DMA

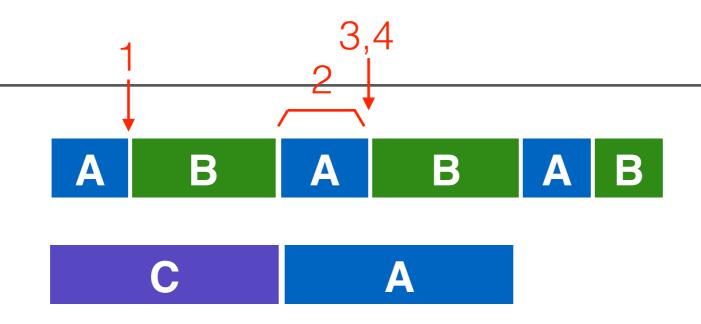
Control: special instructions *vs.* memory-mapped I/O







What else can we optimize?



What else can we optimize? Data transfer!

Programmed I/O vs. Direct Memory Access

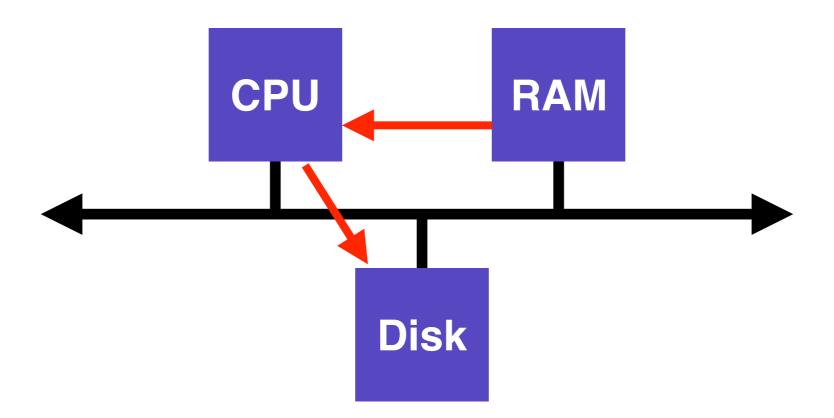
PIO (Programmed I/O):

- CPU directly tells device what data is

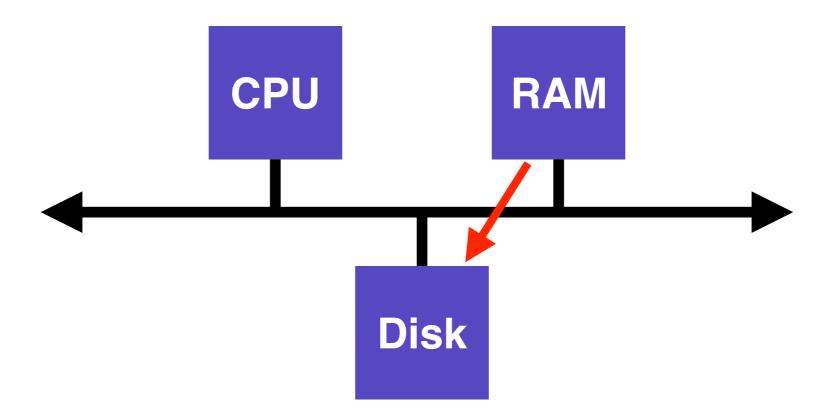
DMA (Direct Memory Access):

- CPU leaves data in memory
- DMA device does copy

PIO Flow



DMA Flow



```
CPU: A B A B

Disk: C A
```

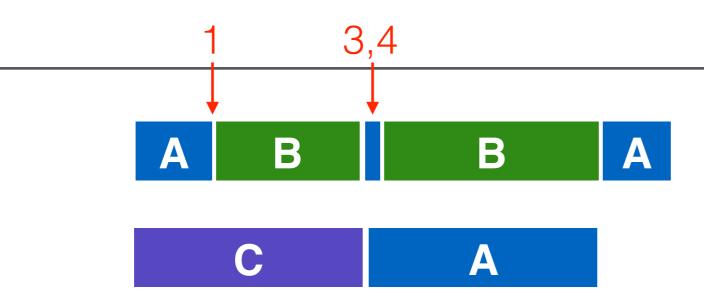
```
2a 2b,3,4
CPU:
            B
                       B
DMA:
                  A
Disk:
                     A
   while (STATUS == BUSY)
                                        // 1
      wait for interrupt;
   initiate DMA transfer
                                        // 2a
                                        // 2b
   wait for interrupt
   Write command to COMMAND
                              register // 3
   while (STATUS == BUSY)
                                        // 4
      wait for interrupt;
```

Protocol Variants

Status checks: polling vs. interrupts

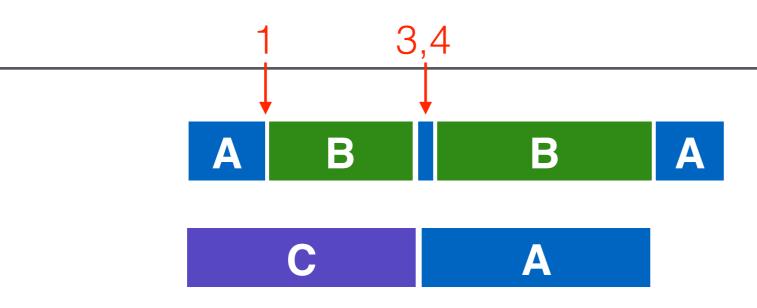
Data: PIO vs. DMA

Control: special instructions *vs.* memory-mapped I/O



```
while (STATUS == BUSY)  // 1
  wait for interrupt;

Write data to DATA register  // 2
Write command to COMMAND register  // 3
while (STATUS == BUSY)  // 4
  wait for interrupt;
```



How does OS read and write registers?

Special Instructions vs. Mem-Mapped I/O

Special instructions

- each device has a port
- in/out instructions (x86) communicate with device

Memory-Mapped I/O

- H/W maps registers into address space
- loads/stores sent to device

Doesn't matter much (both are used).

Variety is a Challenge

Problem:

- many, many devices
- each has its own protocol

How can we avoid writing a slightly different OS for each H/W combination?

Solution

Encapsulation!

Write driver for each device.

Drivers are 70% of Linux source code.

Solution

Encapsulation!

Write driver for each device.

Drivers are 70% of Linux source code.

Encapsulation also enables us to mix-and-match devices, schedulers, and file systems.

Storage Stack

```
file system
scheduler
driver
hard drive
```

Storage Stack

application

file system

scheduler

driver

hard drive

build common interface on top of all HDDs

Storage Stack

what about special capabilities?

application

file system

scheduler

driver

hard drive

build common interface on top of all HDDs

Disk has a sector-addressable address space

Disk has a sector-addressable address space (so a disk is like an array of sectors).

Disk has a sector-addressable address space (so a disk is like an array of sectors).

Disk has a sector-addressable address space (so a disk is like an array of sectors).

Sectors are typically 512 bytes or 4096 bytes.

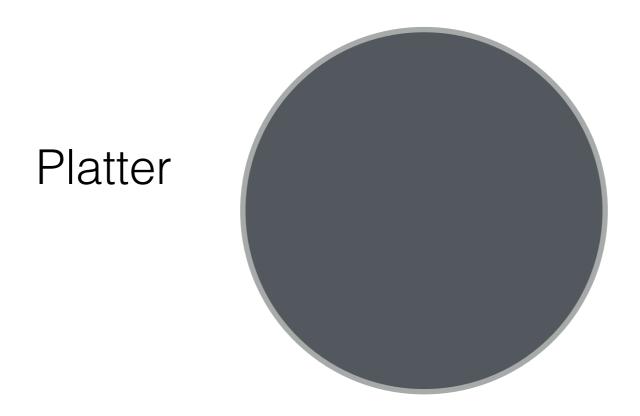
Disk has a sector-addressable address space (so a disk is like an array of sectors).

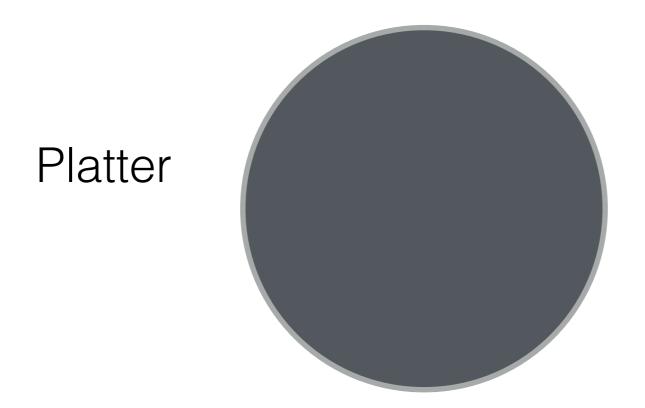
Sectors are typically 512 bytes or 4096 bytes.

Disk has a sector-addressable address space (so a disk is like an array of sectors).

Sectors are typically 512 bytes or 4096 bytes.

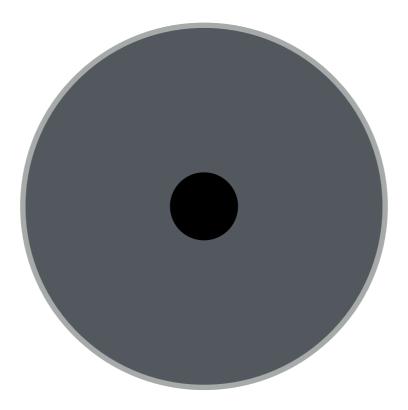
Main operations: reads + writes to sectors (blocks).

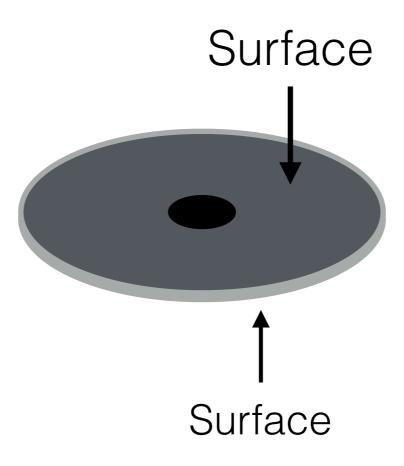


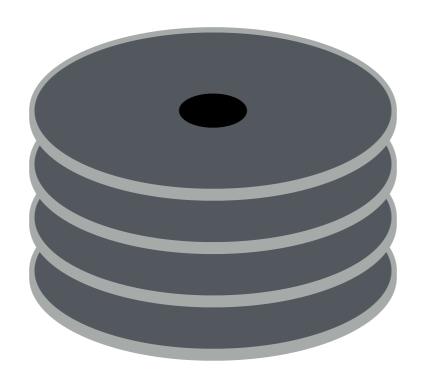


Platter is covered with a magnetic film.

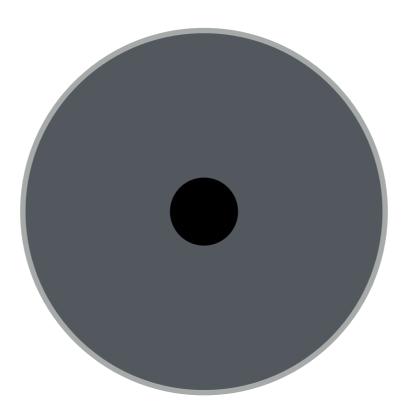
Spindle

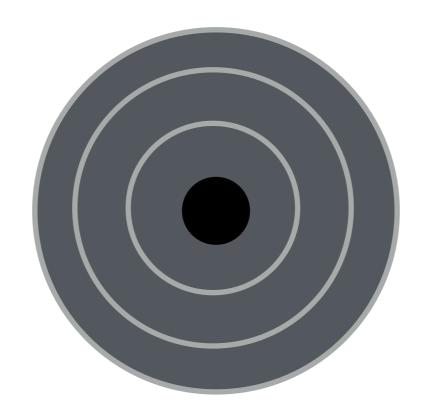




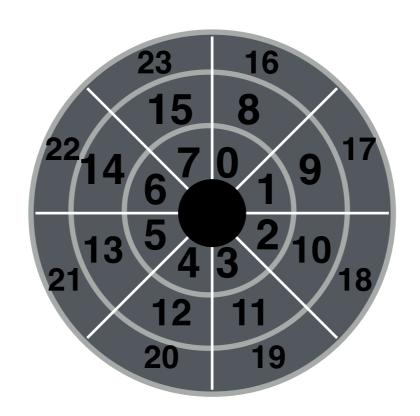


Many platters may be bound to the spindle.

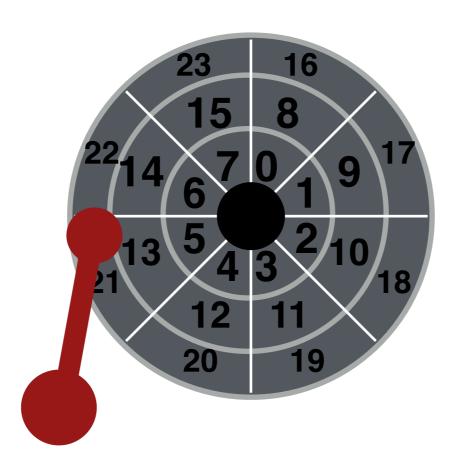




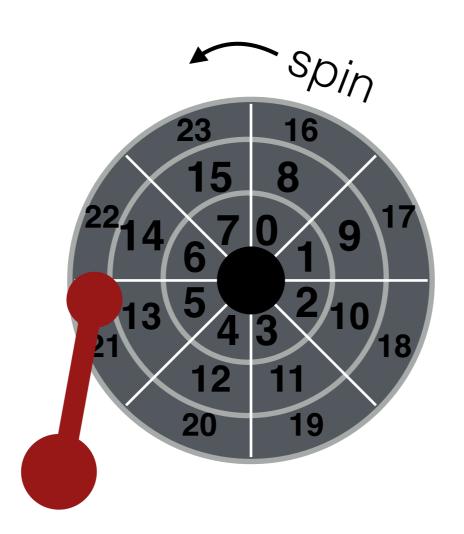
Each surface is divided into rings called <u>tracks</u>. A stack of tracks (across platters) is called a <u>cylinder</u>.



The tracks are divided into numbered sectors.



Heads on a moving arm can read from each surface.

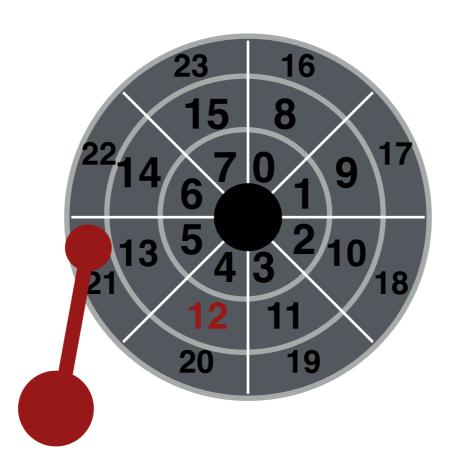


Spindle/platters rapidly spin.

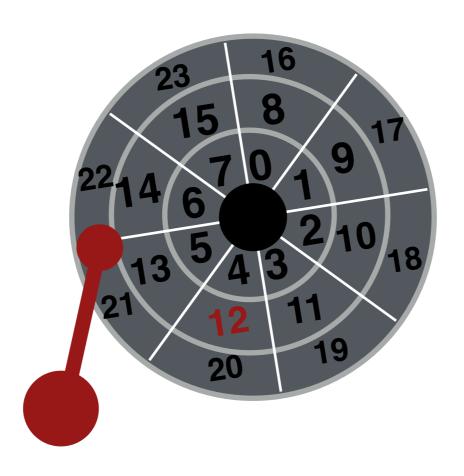
Don't try this at home!

http://youtu.be/9eMWG3fwiEU?t=30s

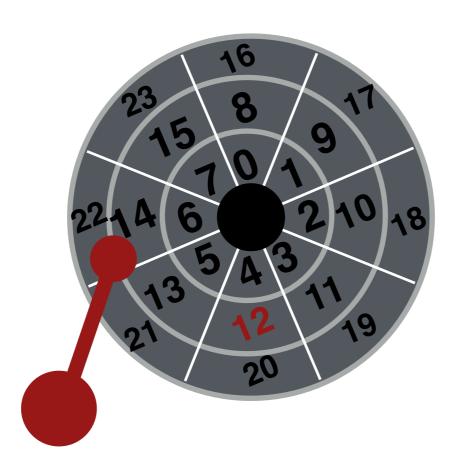
Let's Read 12!



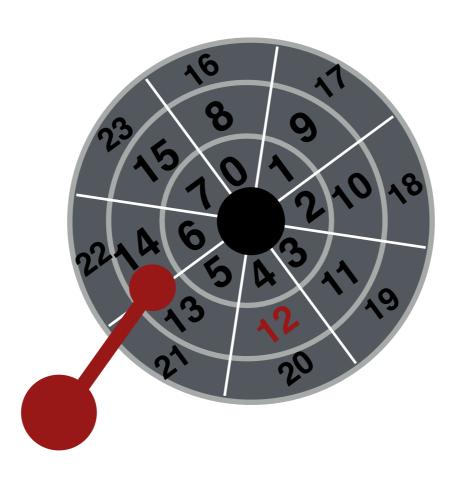
Seek to right track.

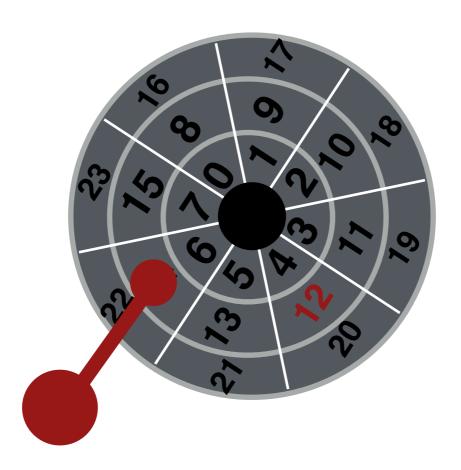


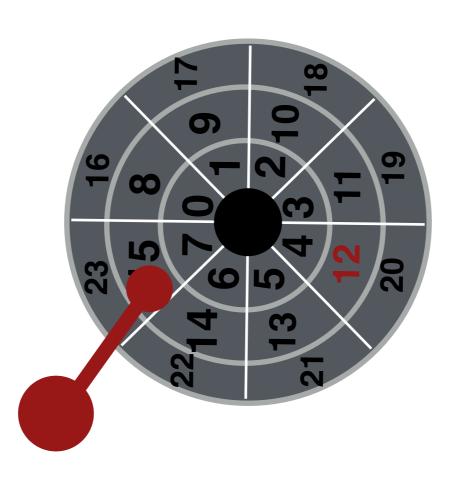
Seek to right track.

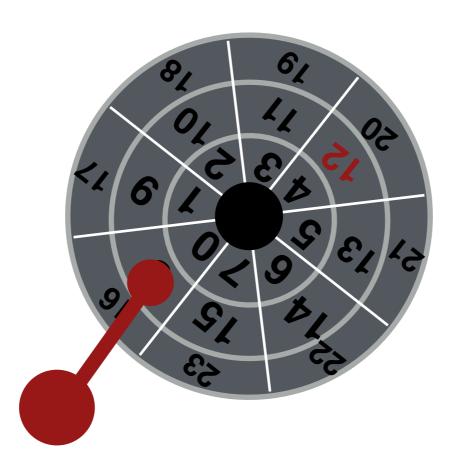


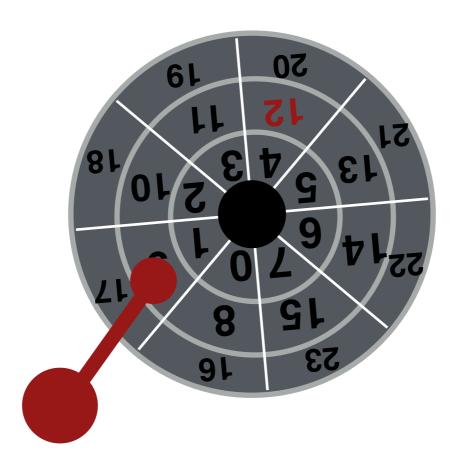
Seek to right track.

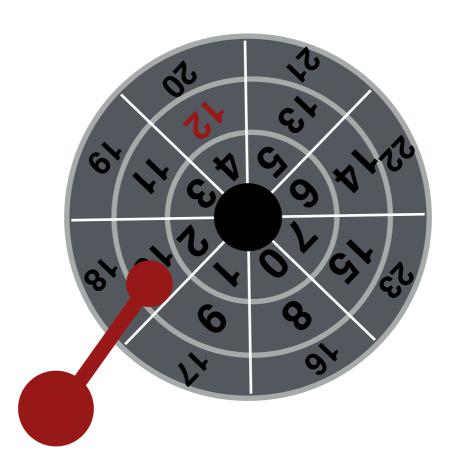


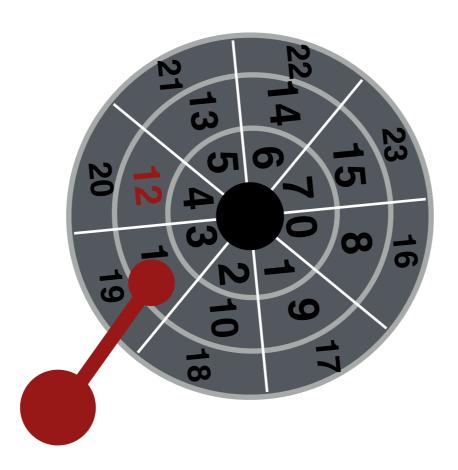




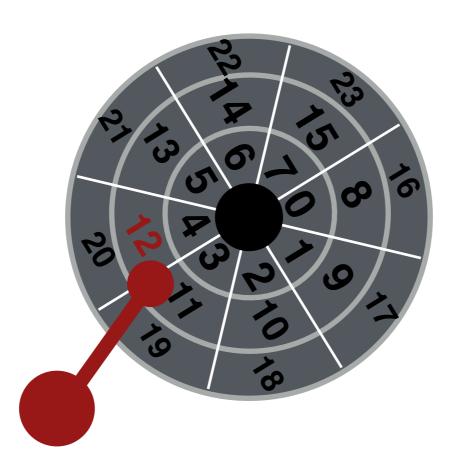




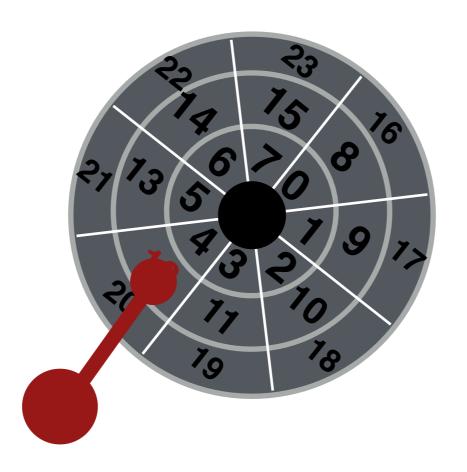




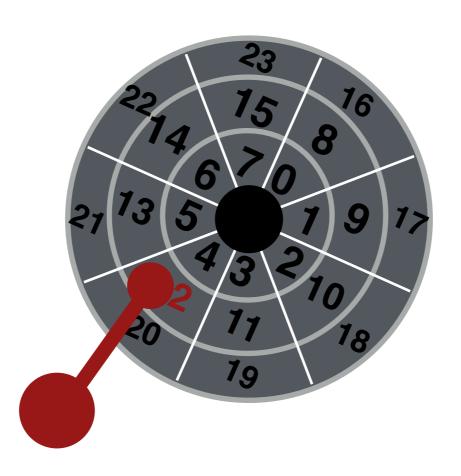
Transfer data.

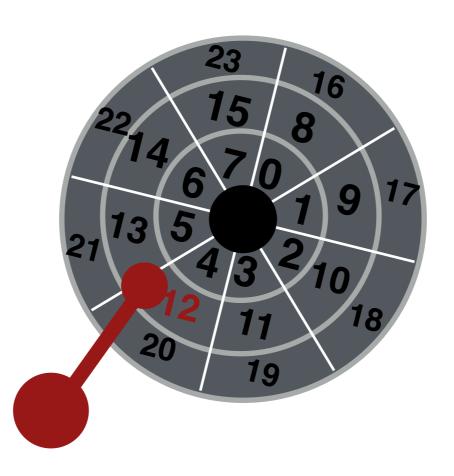


Transfer data.



Transfer data.





Must accelerate, coast, decelerate, settle

Must accelerate, coast, decelerate, settle

Must accelerate, coast, decelerate, settle

Seeks often take several milliseconds!

Must accelerate, coast, decelerate, settle

Seeks often take several milliseconds!

Must accelerate, coast, decelerate, settle

Seeks often take several milliseconds!

Settling alone can take 0.5 - 2 ms.

Must accelerate, coast, decelerate, settle

Seeks often take several milliseconds!

Settling alone can take 0.5 - 2 ms.

Must accelerate, coast, decelerate, settle

Seeks often take several milliseconds!

Settling alone can take 0.5 - 2 ms.

Entire seek often takes 4 - 10 ms.

Depends on rotations per minute (RPM).

- 7200 RPM is common, 15000 RPM is high end.

1 / 7200 RPM =

Depends on rotations per minute (RPM).

```
1 / 7200 RPM =
1 minute / 7200 rotations =
```

Depends on rotations per minute (RPM).

```
1 / 7200 RPM =
1 minute / 7200 rotations =
1 second / 120 rotations =
```

Depends on rotations per minute (RPM).

```
1 / 7200 RPM =
1 minute / 7200 rotations =
1 second / 120 rotations =
12 ms / rotation
```

Depends on rotations per minute (RPM).

- 7200 RPM is common, 15000 RPM is high end.

```
1 / 7200 RPM =
1 minute / 7200 rotations =
1 second / 120 rotations =
12 ms / rotation
```

so it may take **6 ms** on avg to rotate to target (0.5 * 12 ms)

Pretty fast — depends on RPM and sector density.

Pretty fast — depends on RPM and sector density.

Pretty fast — depends on RPM and sector density.

100+ MB/s is typical.

Pretty fast — depends on RPM and sector density.

100+ MB/s is typical.

Seek, Rotate, Transfer

Pretty fast — depends on RPM and sector density.

100+ MB/s is typical.

1s / 100 MB = 10 ms / MB = 4.9 us / sector

Seek, Rotate, Transfer

Pretty fast — depends on RPM and sector density.

100+ MB/s is typical.

1s / 100 MB = 10 ms / MB = 4.9 us / sector (assuming 512-byte sector)

So...

- seeks are slow

- seeks are slow
- rotations are slow

- seeks are slow
- rotations are slow
- transfers are fast

- seeks are slow
- rotations are slow
- transfers are fast

So...

- seeks are slow
- rotations are slow
- transfers are fast

What kind of workload is fastest for disks?

So...

- seeks are slow
- rotations are slow
- transfers are fast

What kind of workload is fastest for disks?

So...

- seeks are slow
- rotations are slow
- transfers are fast

What kind of workload is fastest for disks?

Sequential: access sectors in order (transfer dominated)

Random: access sectors arbitrarily (seek+rotation dominated)

Demos: example-rand.csh and example-seq.csh

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

Sequential workload: what is throughput for each?

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

Cheeta: 125 MB/s.

Barracuda: 105 MB/s.

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

Random workload: what is throughput for each? (what else do you need to know?)

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

Random workload: what is throughput for each? Assume 16-KB reads.

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

Random workload: what is throughput for each? Assume 16-KB reads.

	Cheetah	Barracuda	
RPM	15,000	7,200	
Avg Seek	4 ms	9 ms	
Max Transfer	125 MB/s	105 MB/s	

	Cheetah	Barracuda	
RPM	15,000	7,200	
Avg Seek	4 ms	9 ms	
Max Transfer	125 MB/s	105 MB/s	

avg rotation =
$$\frac{1}{2} \times \frac{1 \text{ min}}{15000}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

avg rotation =
$$\frac{1}{2} \times \frac{1 \text{ min}}{15000} \times \frac{60 \text{ sec}}{1 \text{ min}} \times \frac{1000 \text{ ms}}{1 \text{ sec}} = 2 \text{ ms}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

transfer =
$$\frac{1 \text{ sec}}{125 \text{ MB}} \times 16 \text{ KB} \times \frac{1,000,000 \text{ us}}{1 \text{ sec}} = 125 \text{ us}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Cheetah time = 4ms + 2ms + 125us = 6.1ms

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Cheetah time = 4ms + 2ms + 125us = 6.1ms

throughput =
$$\frac{16 \text{ KB}}{6.1 \text{ms}}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Cheetah time = 4ms + 2ms + 125us = 6.1ms

throughput =
$$\frac{16 \text{ KB}}{6.1 \text{ms}} \times \frac{1 \text{ MB}}{1024 \text{ KB}} \times \frac{100 \text{ ms}}{1 \text{ sec}} = 2.5 \text{ MB/s}$$

	Cheetah	Barracuda	
RPM	15,000	7,200	
Avg Seek	4 ms	9 ms	
Max Transfer	125 MB/s	105 MB/s	

	Cheetah	Barracuda	
RPM	15,000	7,200	
Avg Seek	4 ms	9 ms	
Max Transfer	125 MB/s	105 MB/s	

avg rotation =
$$\frac{1}{2} \times \frac{1 \text{ min}}{7200} \times \frac{60 \text{ sec}}{1 \text{ min}} \times \frac{1000 \text{ ms}}{1 \text{ sec}} = 4.1 \text{ ms}$$

	Cheetah	Barracuda	
RPM	15,000	7,200	
Avg Seek	4 ms	9 ms	
Max Transfer	125 MB/s	105 MB/s	

transfer =
$$\frac{1 \text{ sec}}{105 \text{ MB}} \times 16 \text{ KB} \times \frac{1,000,000 \text{ us}}{1 \text{ sec}} = 149 \text{ us}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Barracuda time = 9ms + 4.1ms + 149us = 13.2ms

throughput =
$$\frac{16 \text{ KB}}{13.2 \text{ms}} \times \frac{1 \text{ MB}}{1024 \text{ KB}} \times \frac{1000 \text{ ms}}{1 \text{ sec}}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Barracuda time = 9ms + 4.1ms + 149us = 13.2ms

throughput =
$$\frac{16 \text{ KB}}{13.2 \text{ms}} \times \frac{1 \text{ MB}}{1024 \text{ KB}} \times \frac{1000 \text{ ms}}{1 \text{ sec}} = 1.2 \text{ MB/s}$$