Operating Systems I/O devices

Nipun Batra

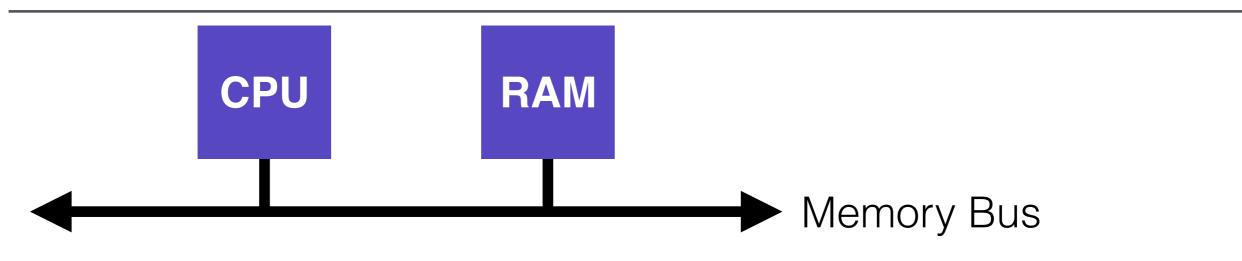
What good is a computer without any I/O devices? - keyboard, display, disks

We want:

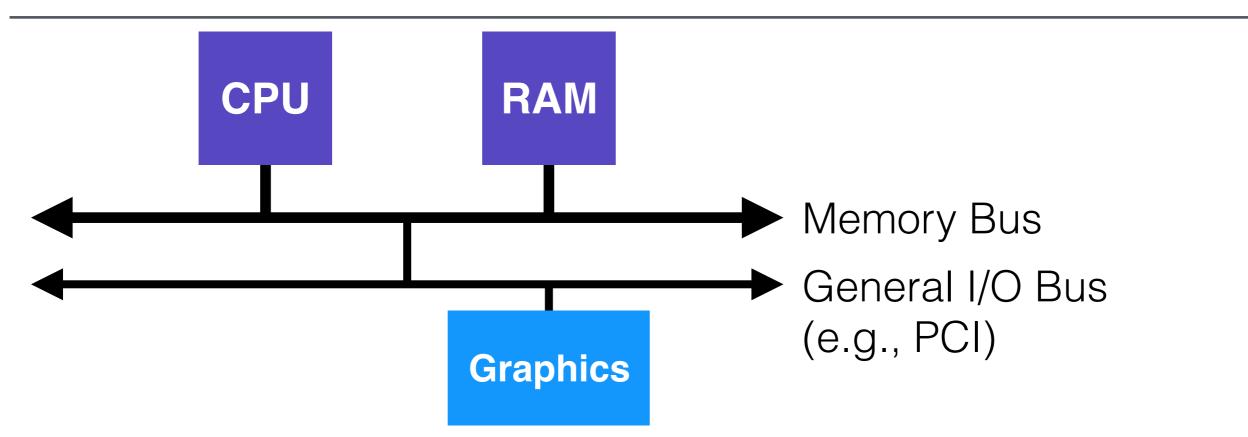
- H/W that will let us plug in different devices
- OS that can interact with different combinations

Largely a communication problem...

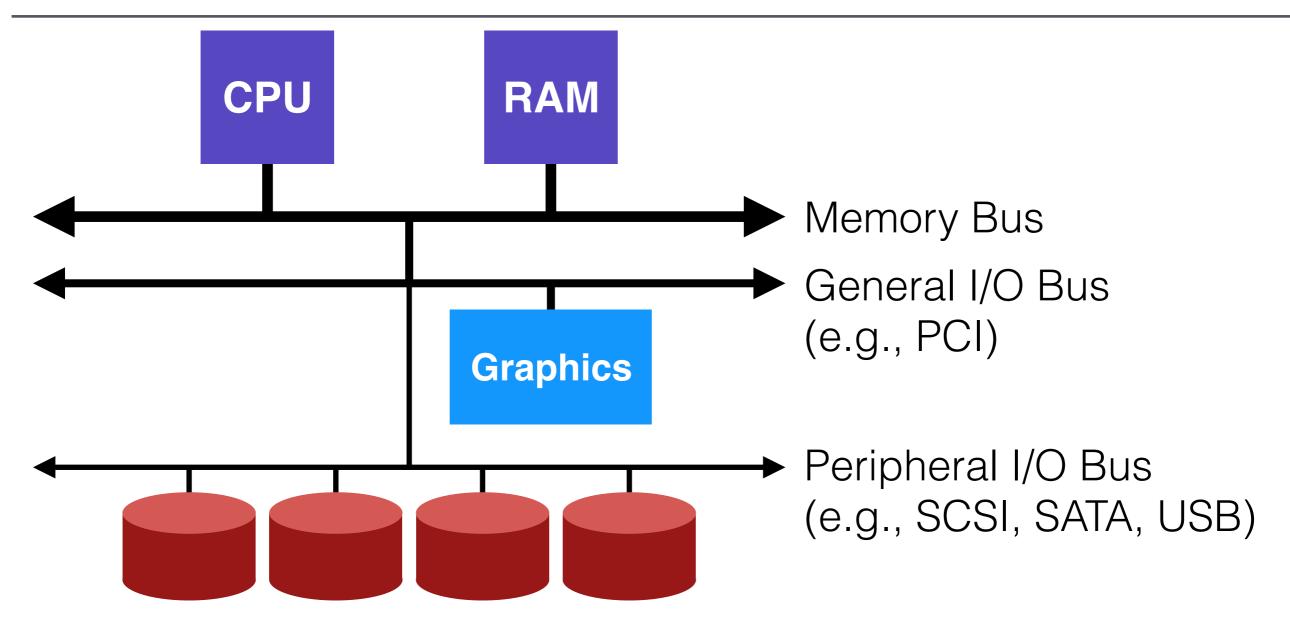
System Architecture



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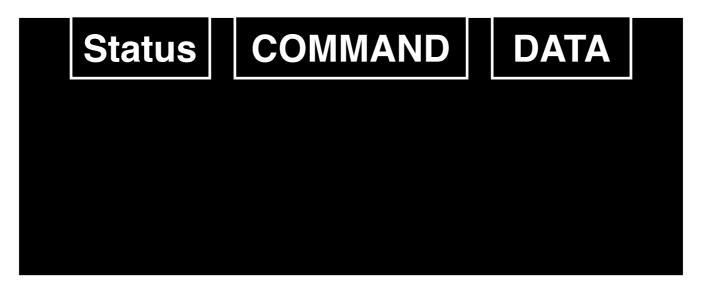


System Architecture



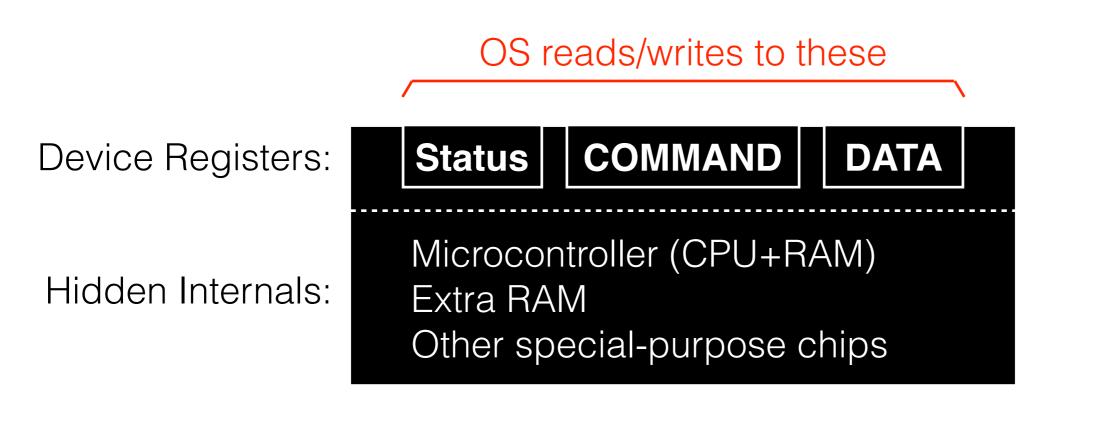
Why use hierarchical buses?

Device Registers:









```
while (STATUS == BUSY)
  ; // spin
Write data to DATA register
Write command to COMMAND register
while (STATUS == BUSY)
  ; // spin
```

CPU:

Disk:

```
while (STATUS == BUSY) // 1
;
Write data to DATA register // 2
Write command to COMMAND register // 3
while (STATUS == BUSY) // 4
;
```

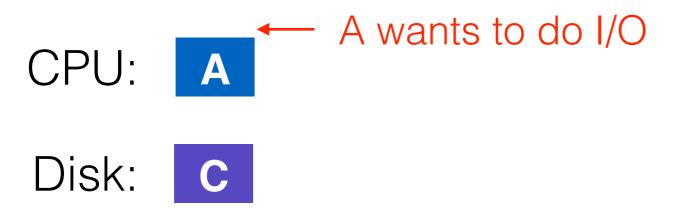




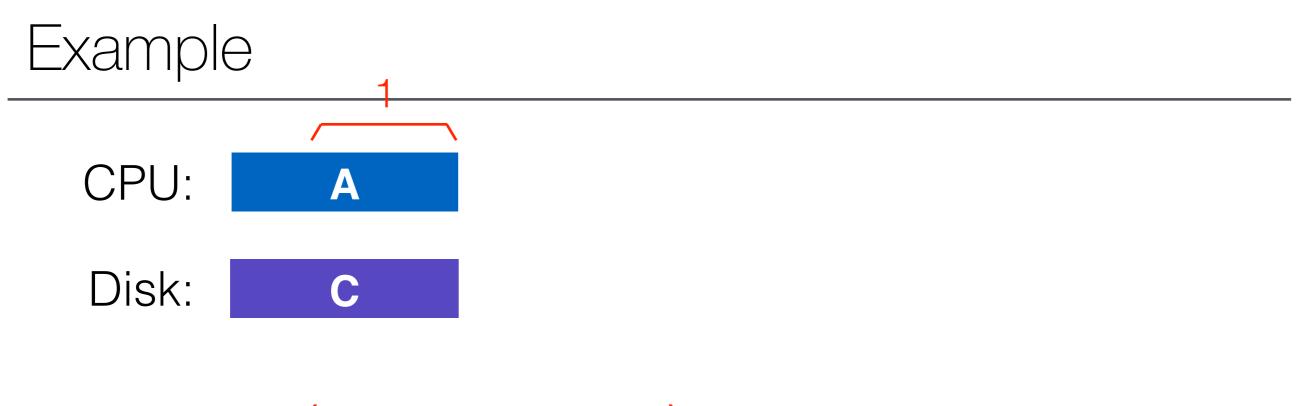
Disk: C

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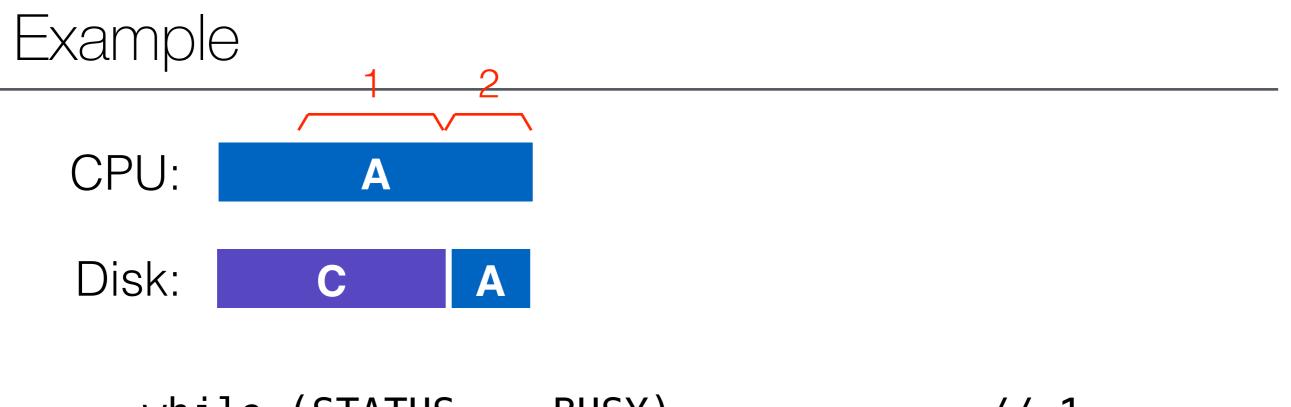




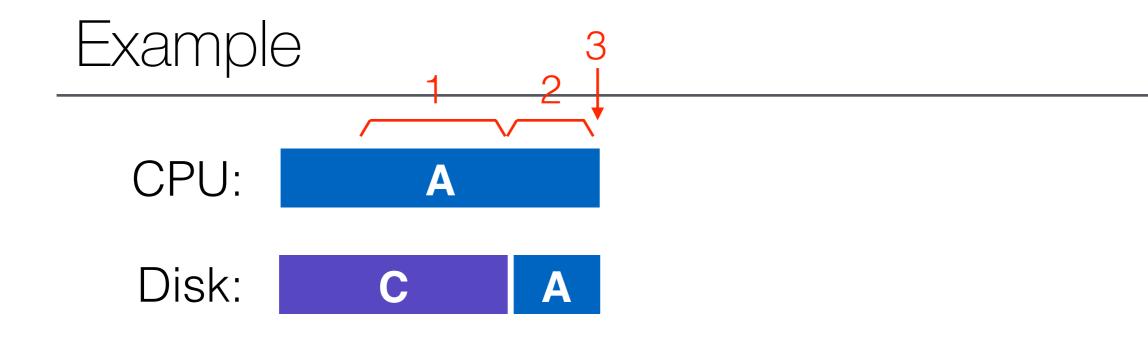
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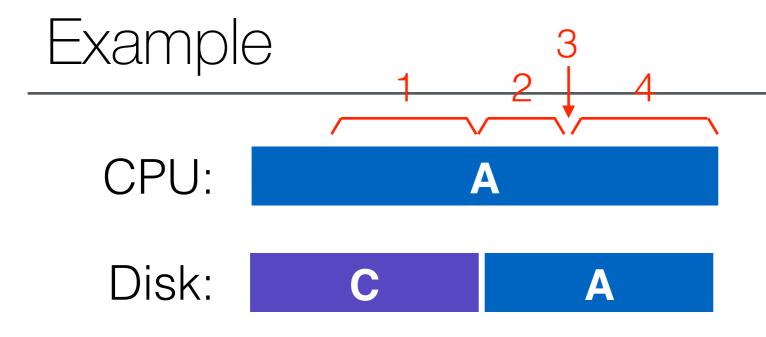
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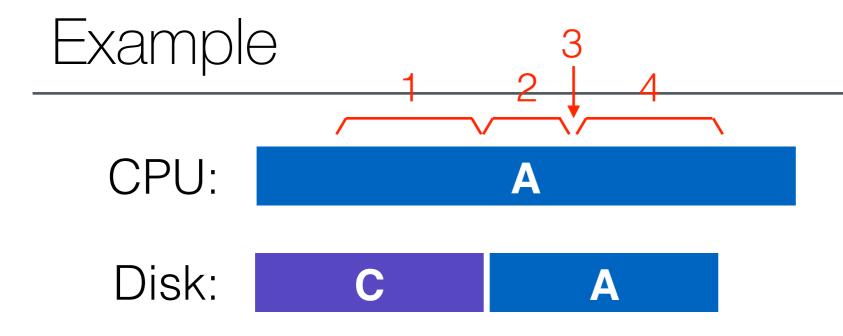
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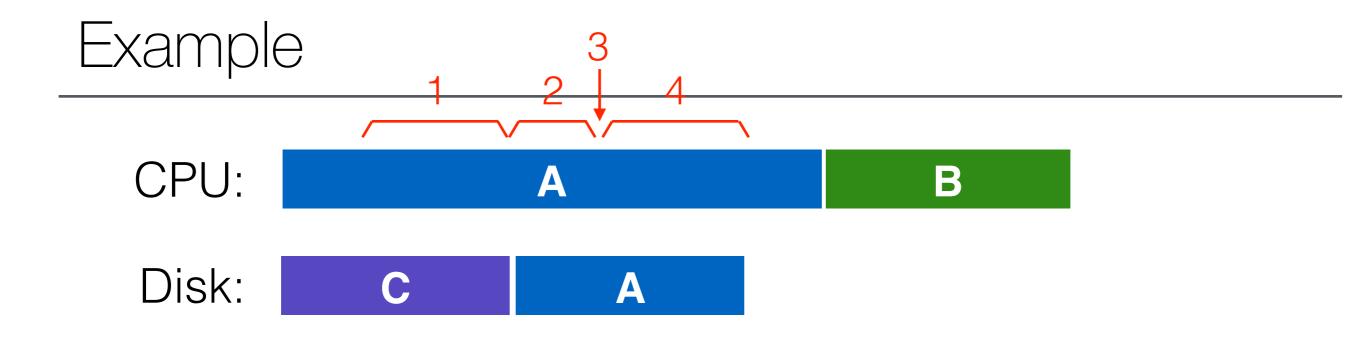
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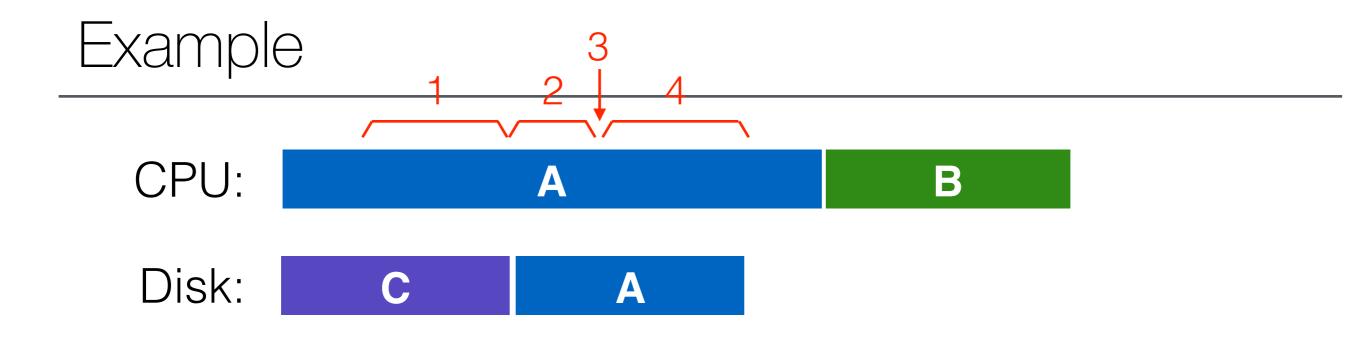
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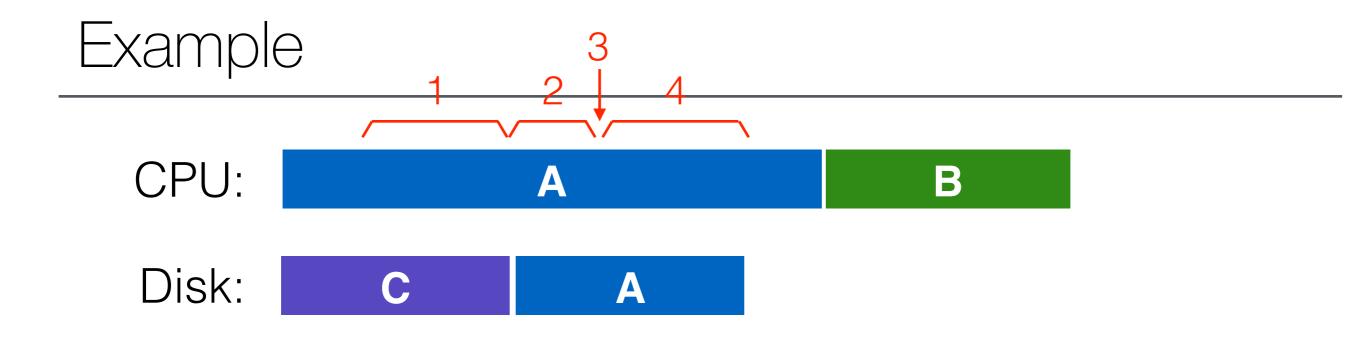


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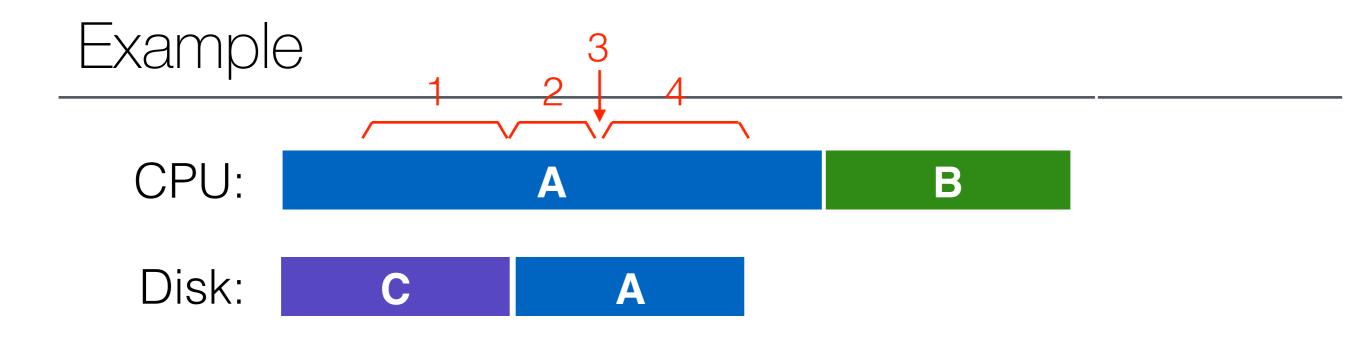
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How to avoid spinning?

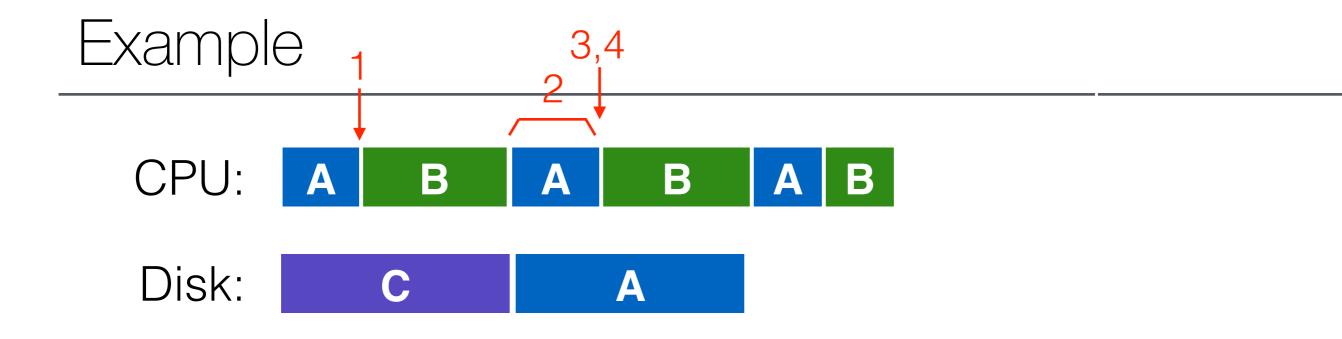


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How to avoid spinning? Interrupts!



while (STATUS == BUSY) // 1
wait for interrupt;
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wait for interrupt;



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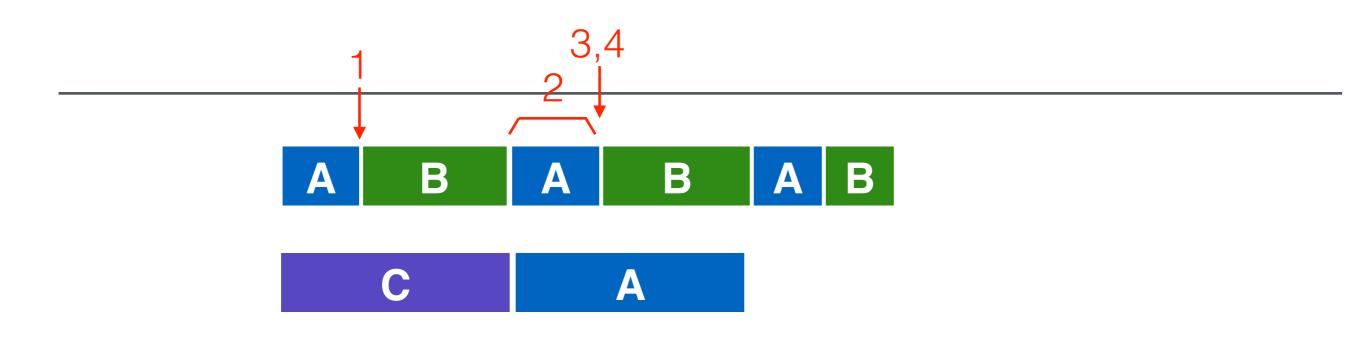
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 - interrupt coalescing

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 - e.g., flood of network packets
- Techniques:
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 - Poll for a while, then wait for interrupts
 - interrupt coalescing
 - Coalesce or combine the delivery of multiple interrupts

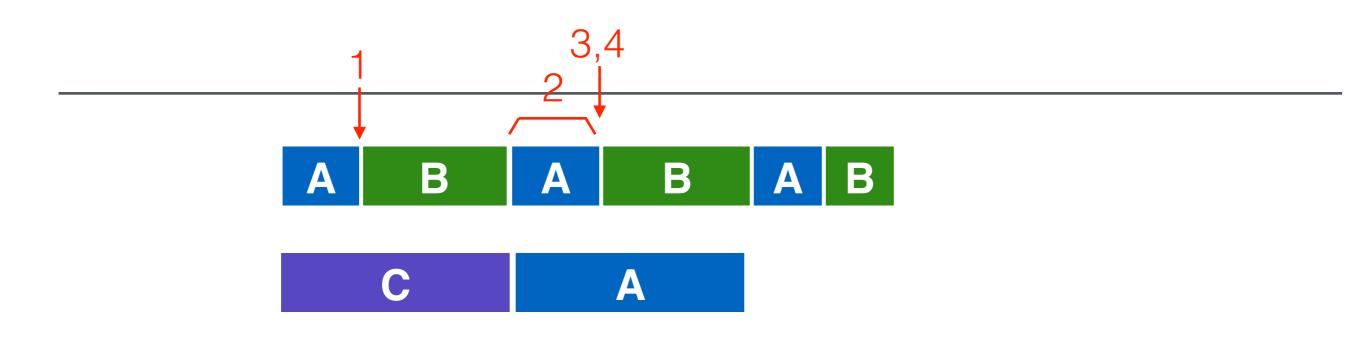
Status checks: polling vs. interrupts

Data: PIO vs. DMA

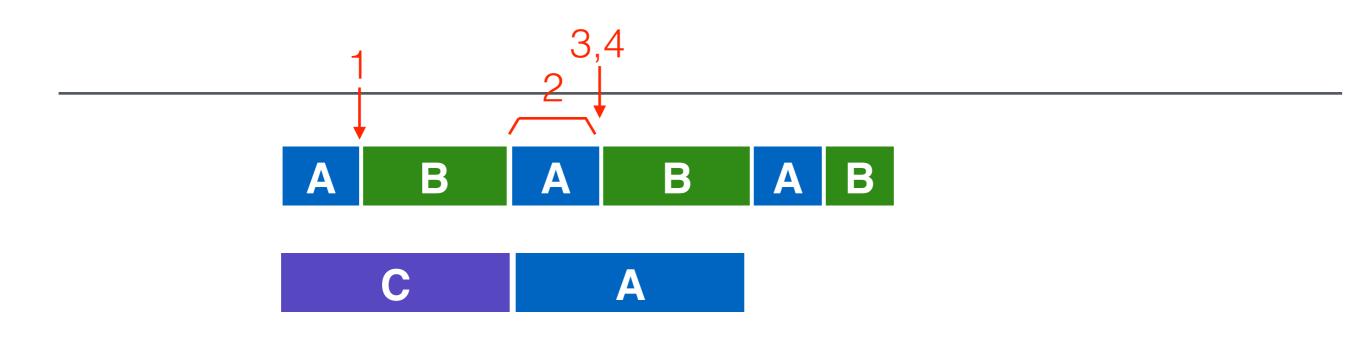
Control: special instructions *vs.* memory-mapped I/O



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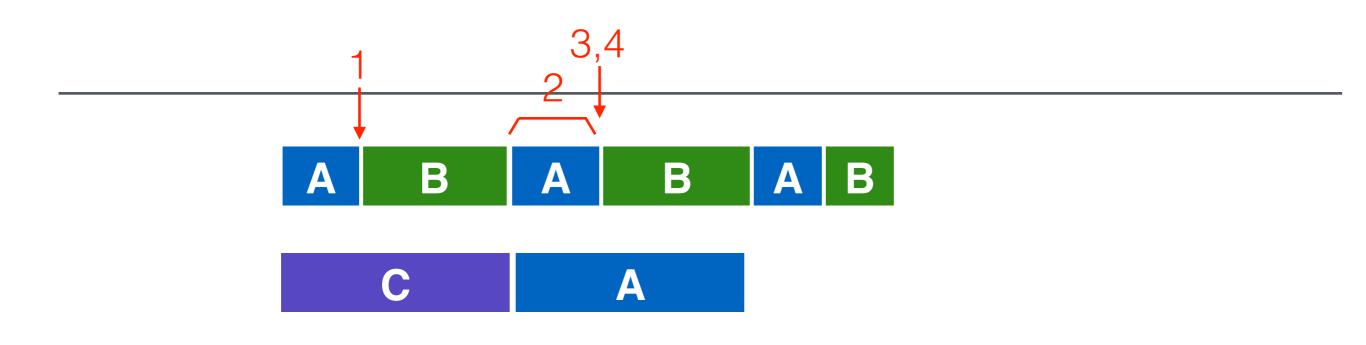


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What else can we optimize?



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```

What else can we optimize? Data transfer!

Programmed I/O vs. Direct Memory Access

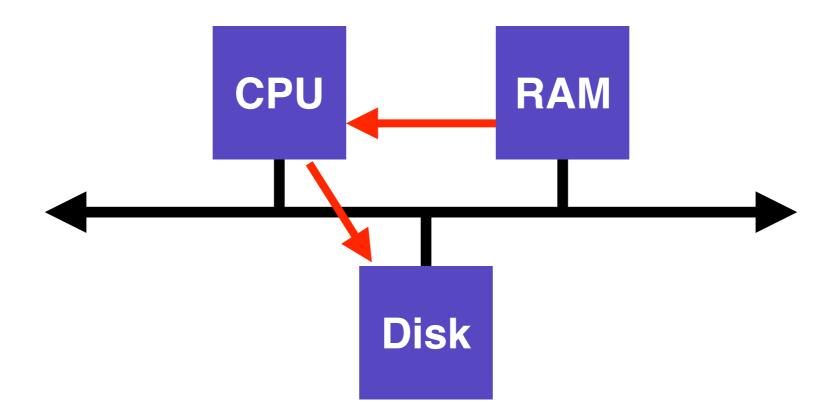
PIO (Programmed I/O):

- CPU directly tells device what data is

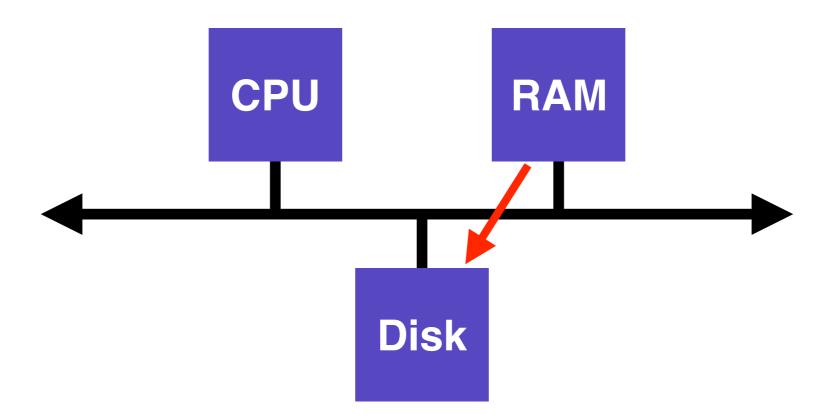
DMA (Direct Memory Access):

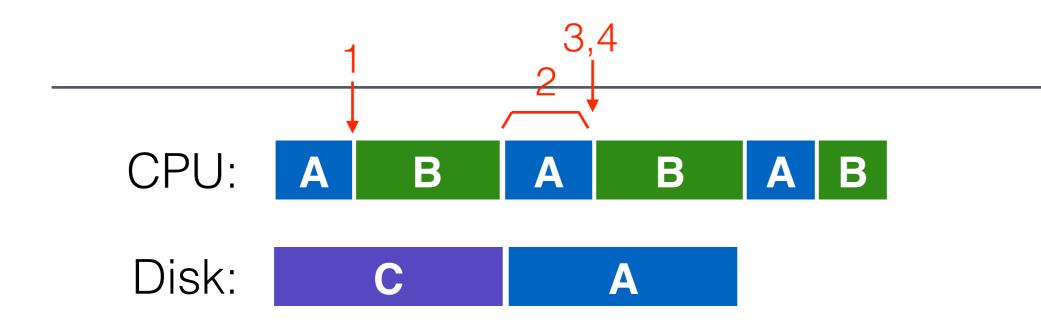
- CPU leaves data in memory
- DMA device does copy



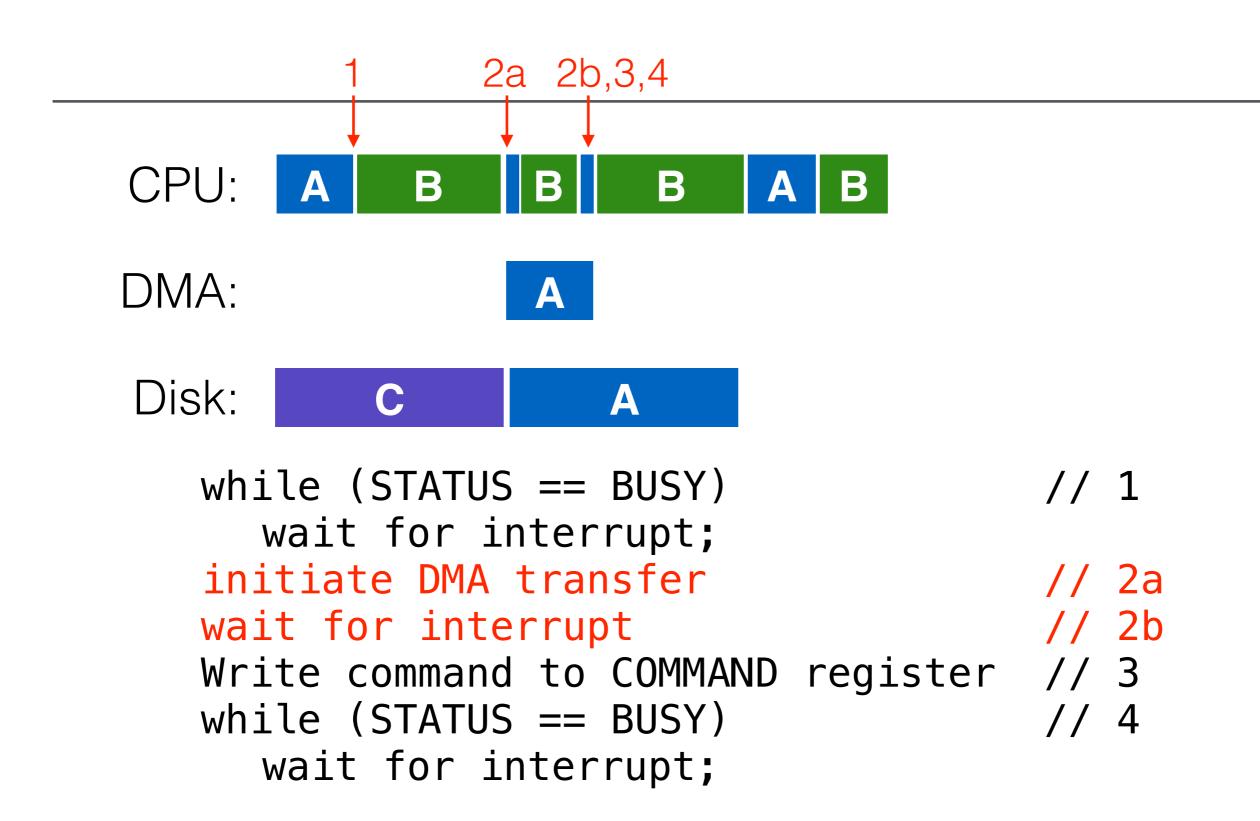








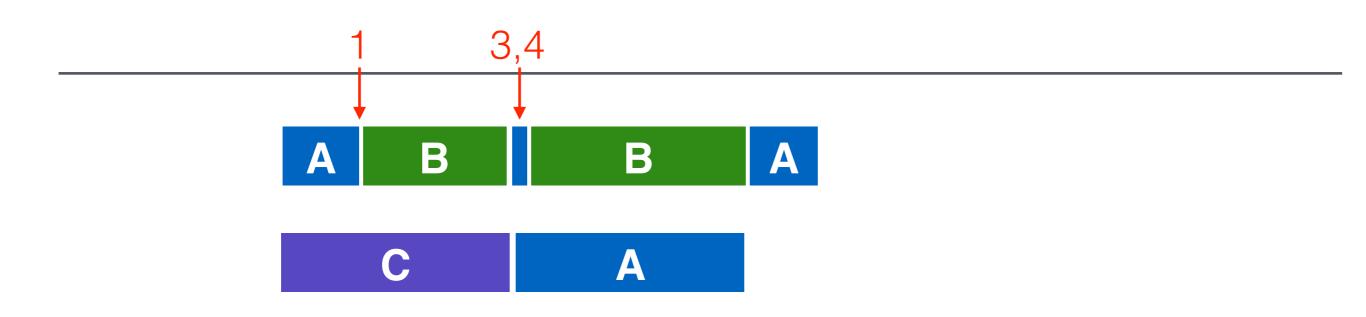
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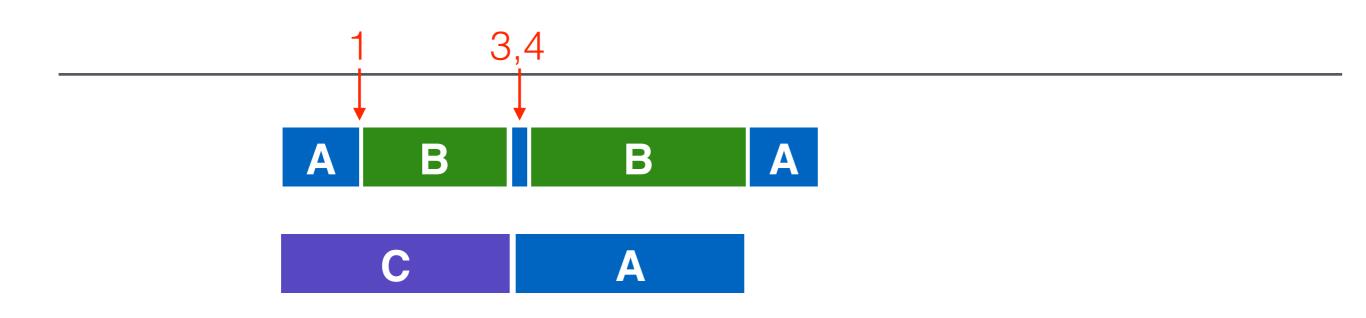
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How does OS read and write registers?

Special Instructions vs. Mem-Mapped I/O

Special instructions

- each device has a port
- in/out instructions (x86) communicate with device

Memory-Mapped I/O

- H/W maps registers into address space
- loads/stores sent to device

Doesn't matter much (both are used).

Variety is a Challenge

Problem:

- many, many devices
- each has its own protocol

How can we avoid writing a slightly different OS for each H/W combination?

Encapsulation!

Write driver for each device.

Drivers are **70%** of Linux source code.

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Encapsulation also enables us to mix-and-match devices, schedulers, and file systems.

Storage Stack

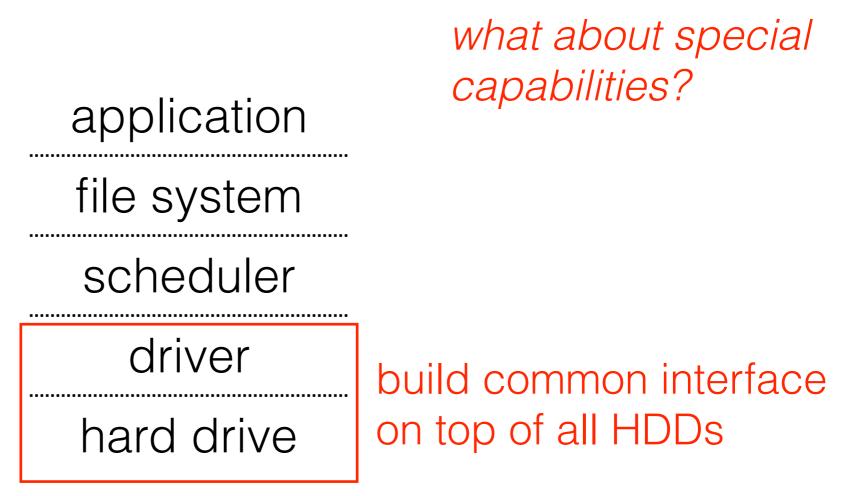
application file system scheduler driver hard drive

Storage Stack

application file system scheduler driver hard drive build cor on top of

build common interface on top of all HDDs

Storage Stack



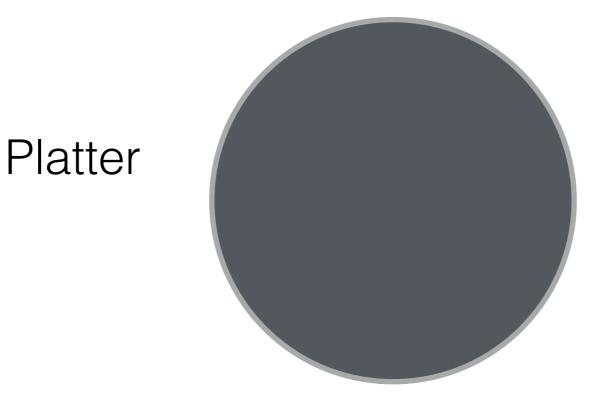
Disk has a sector-addressable address space

Sectors are typically <u>512 bytes</u> or 4096 bytes.

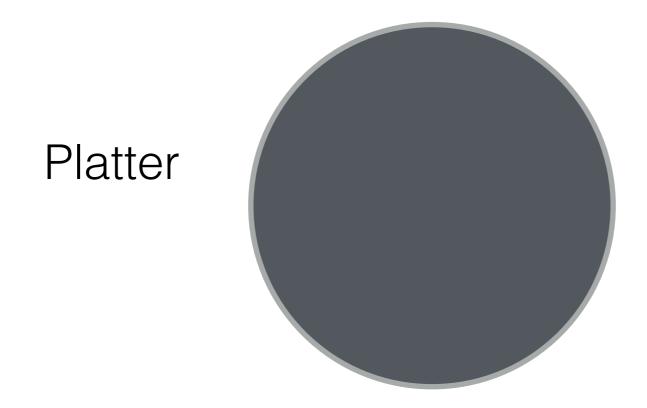
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Main operations: reads + writes to sectors (blocks).

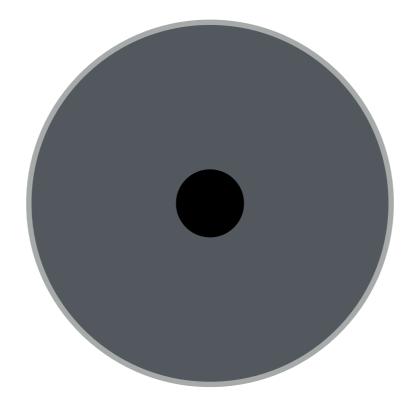


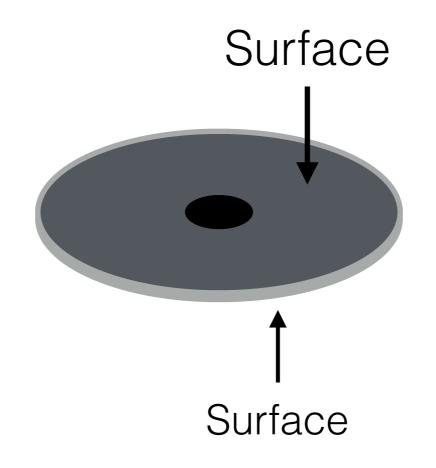
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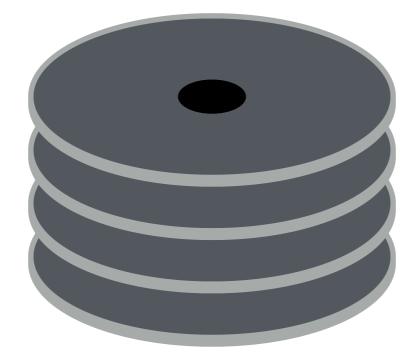


Platter is covered with a magnetic film.

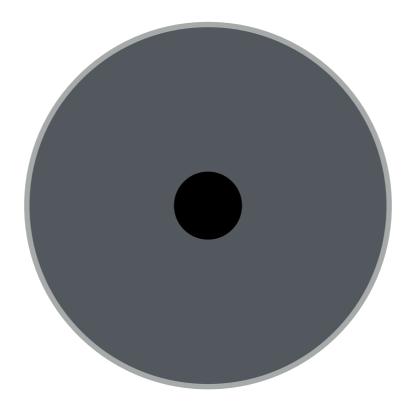
Spindle

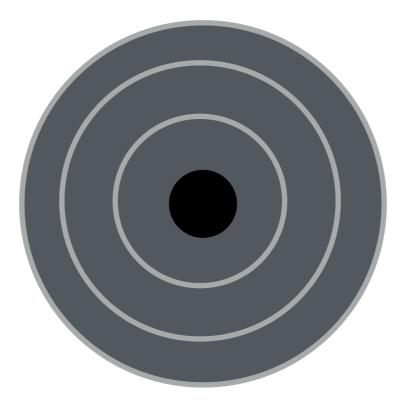




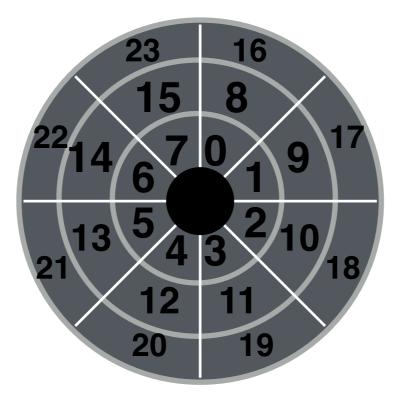


Many platters may be bound to the spindle.

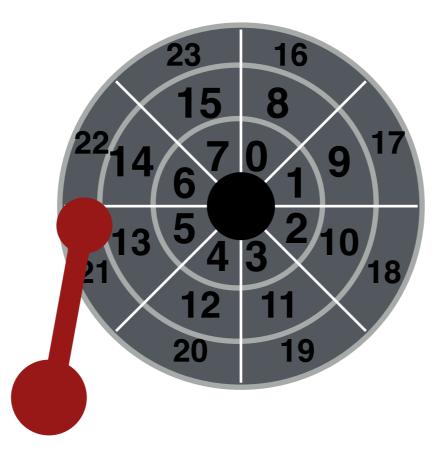




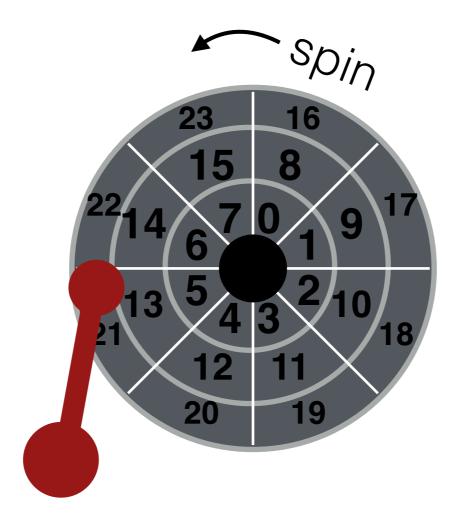
Each surface is divided into rings called <u>tracks</u>. A stack of tracks (across platters) is called a <u>cylinder</u>.



The tracks are divided into numbered sectors.



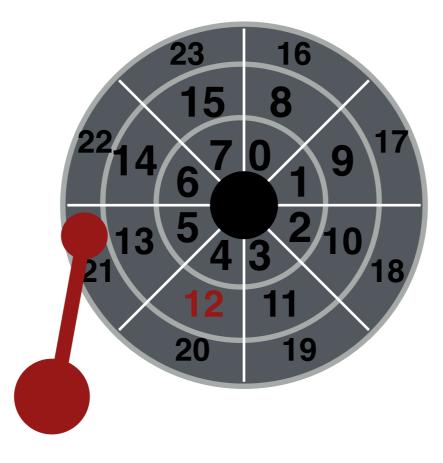
Heads on a moving arm can read from each surface.



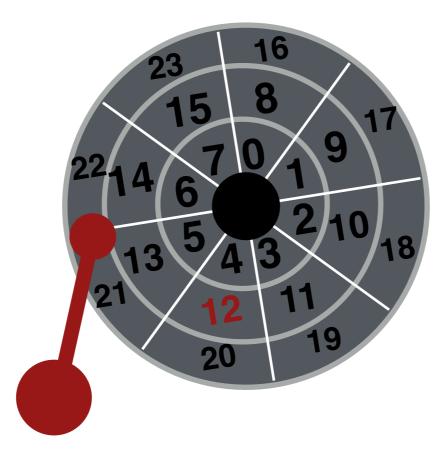
Spindle/platters rapidly spin.

http://youtu.be/9eMWG3fwiEU?t=30s

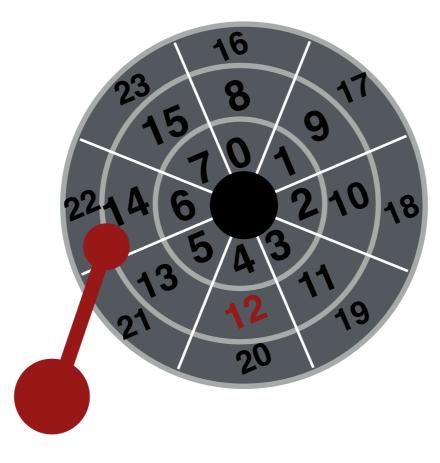
Let's Read 12!



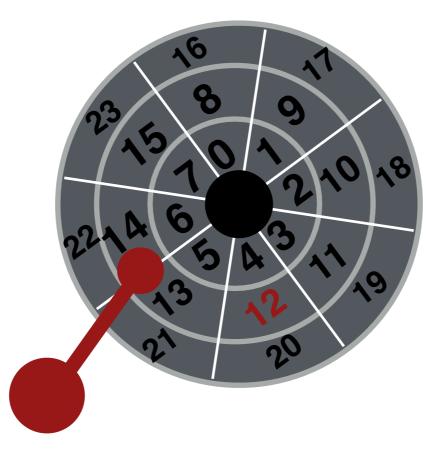
Seek to right track.

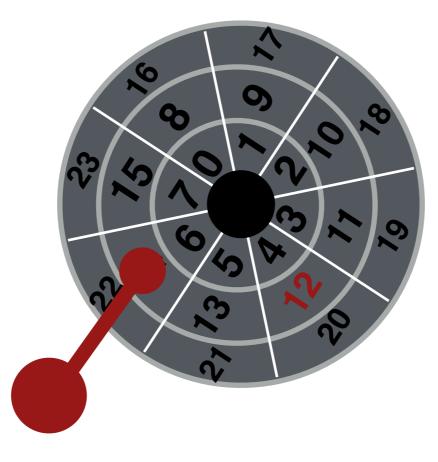


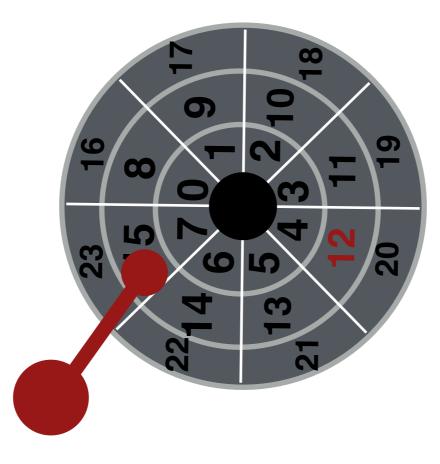
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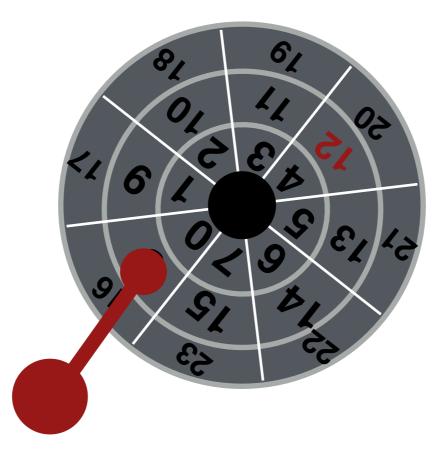


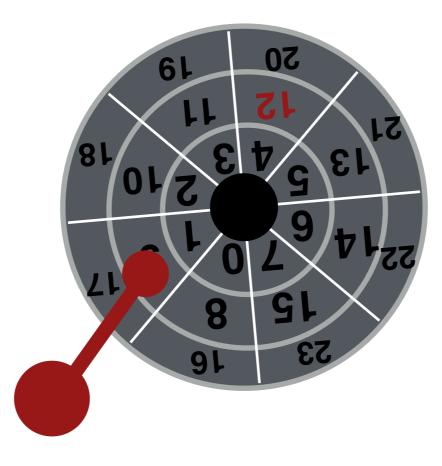
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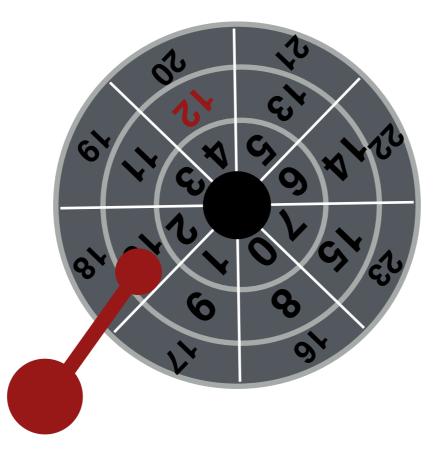


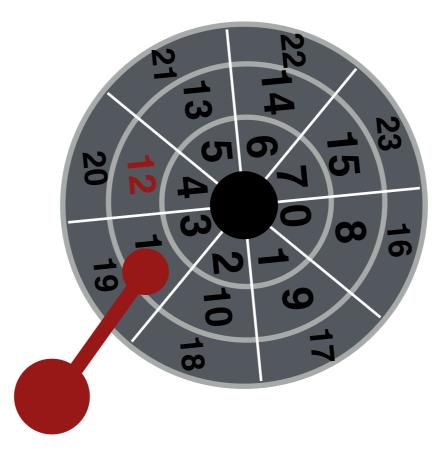




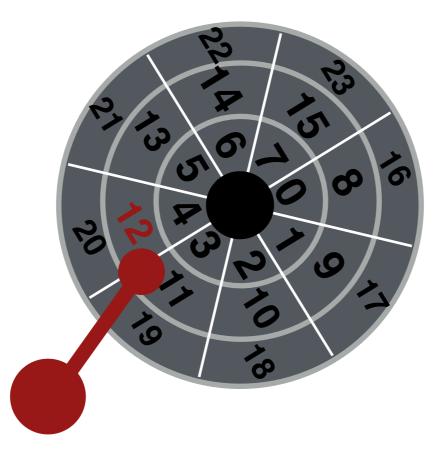




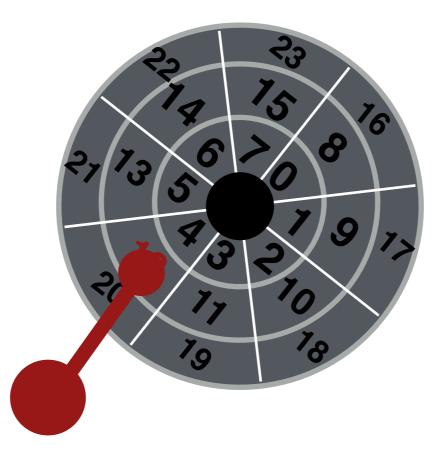




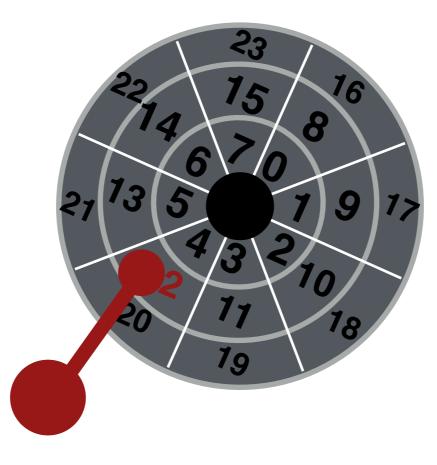
Transfer data.



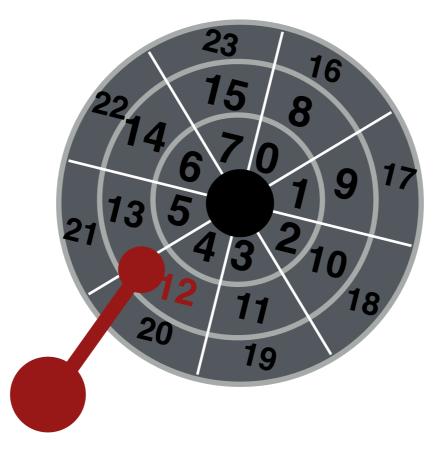
Transfer data.



Transfer data.



Yay!



Must accelerate, coast, decelerate, settle

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Seeks often take several milliseconds!

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Settling alone can take 0.5 - 2 ms.

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Entire seek often takes 4 - 10 ms.

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so it may take **6 ms** on avg to rotate to target (0.5 * 12 ms)

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So...

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What kind of workload is fastest for disks? Sequential: access sectors in order (transfer dominated) Random: access sectors arbitrarily (seek+rotation dominated)

Demos: example-rand.csh and example-seq.csh

	Cheetah	Barracuda
Capacity	300 GB	1 TB
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s
Platters	4	4
Cache	16 MB	32 MB

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Sequential workload: what is throughput for each?

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Cheeta: 125 MB/s. Barracuda: 105 MB/s.

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Random workload: what is throughput for each? (what else do you need to know?)

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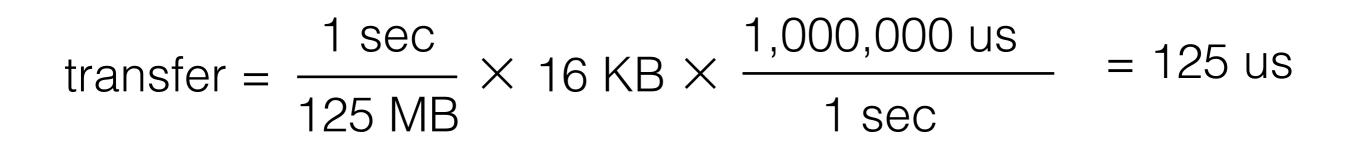
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avg rotation =
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$$\frac{1}{2} \times \frac{1 \text{ min}}{15000} \times \frac{60 \text{ sec}}{1 \text{ min}} \times \frac{1000 \text{ ms}}{1 \text{ sec}} = 2 \text{ ms}$$

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Cheetah time = 4ms + 2ms + 125us = 6.1ms

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Cheetah time = 4ms + 2ms + 125us = 6.1ms throughput = $\frac{16 \text{ KB}}{6.1 \text{ ms}} \times \frac{1 \text{ MB}}{1024 \text{ KB}} \times \frac{100 \text{ ms}}{1 \text{ sec}} = 2.5 \text{ MB/s}$

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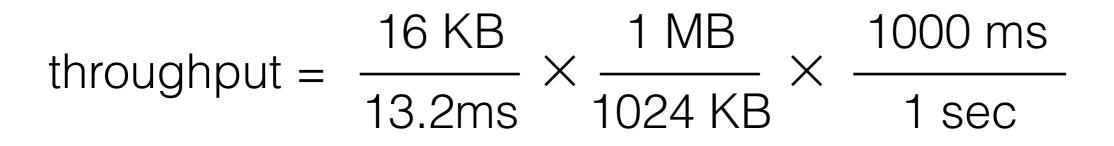
avg rotation =
$$\frac{1}{2} \times \frac{1 \text{ min}}{7200} \times \frac{60 \text{ sec}}{1 \text{ min}} \times \frac{1000 \text{ ms}}{1 \text{ sec}} = 4.1 \text{ ms}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

transfer =
$$\frac{1 \text{ sec}}{105 \text{ MB}} \times 16 \text{ KB} \times \frac{1,000,000 \text{ us}}{1 \text{ sec}} = 149 \text{ us}$$

	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Barracuda time = 9ms + 4.1ms + 149us = 13.2ms



	Cheetah	Barracuda
RPM	15,000	7,200
Avg Seek	4 ms	9 ms
Max Transfer	125 MB/s	105 MB/s

Barracuda time = 9ms + 4.1ms + 149us = 13.2ms

