# Operating Systems Memory Virtualisation Base & Bounds + Segmentation

Kernel

CPU

MMU



Kernel

CPU

MMU

Physical Memory

Virtual Address



Kernel

MMU

CPU

Physical Memory

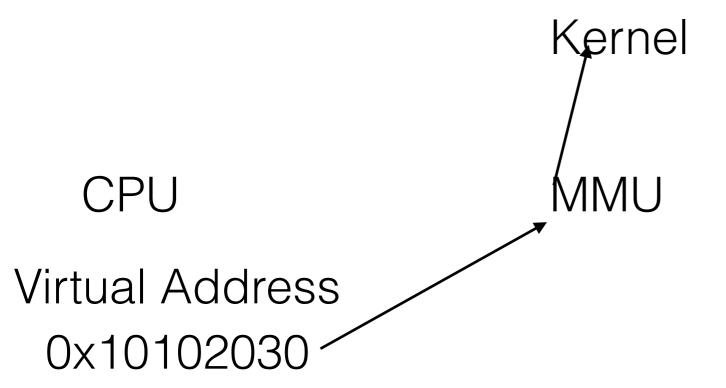
Virtual Address 0x10102030



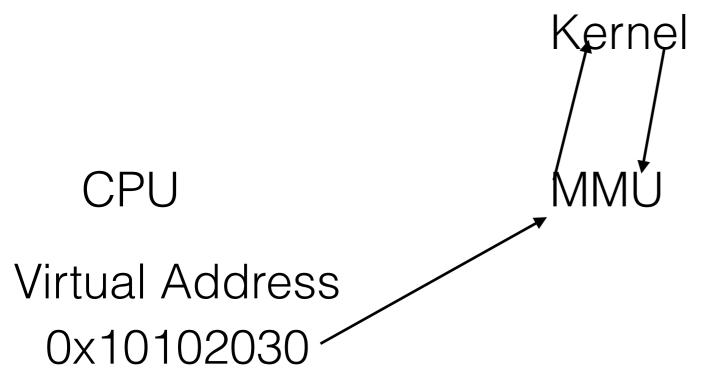
Kernel

CPU MMU
Virtual Address
0x10102030









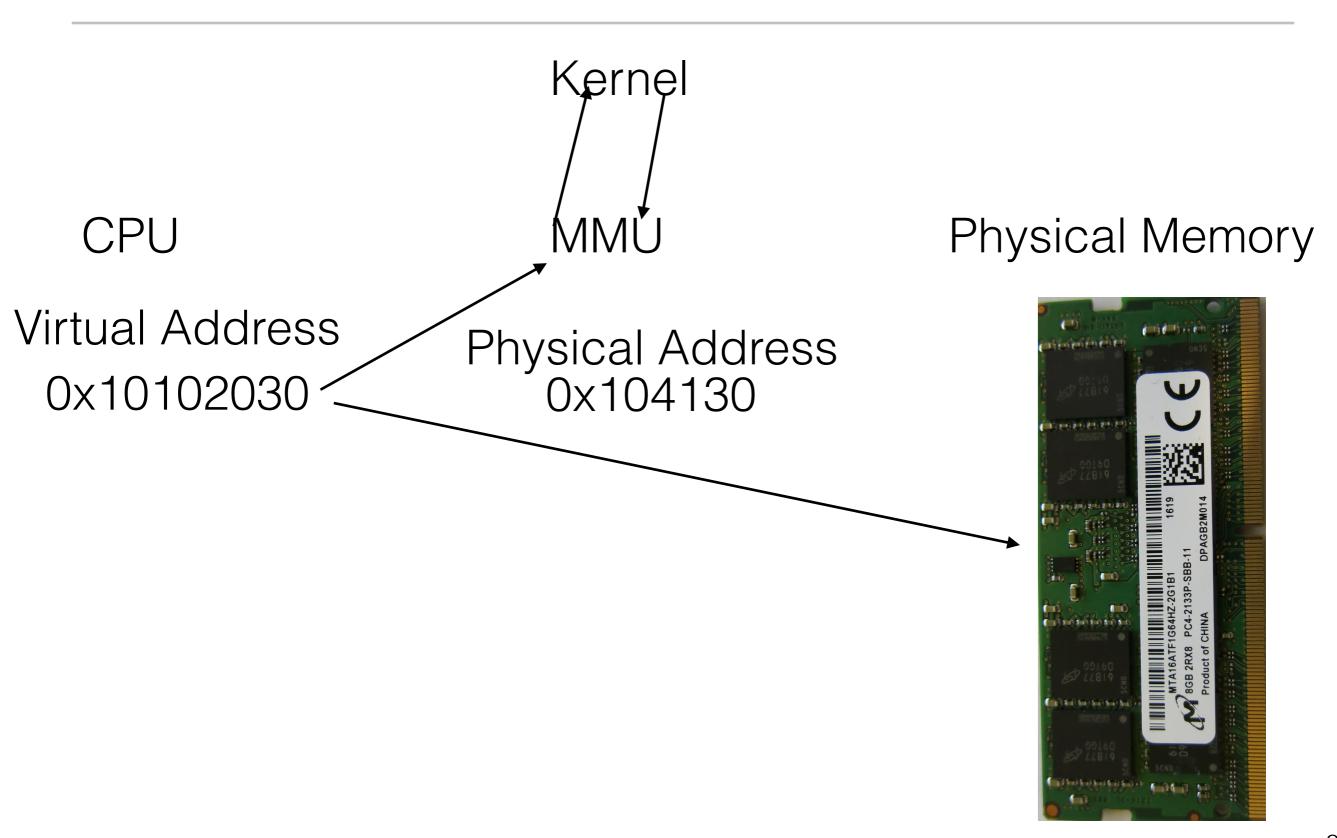


CPU MMU
Virtual Address
0x10102030 Physical Address



CPU MMU
Virtual Address
0x10102030 Physical Address
0x104130





Kernel **CPU** Virtual Address Physical Address 0x104130 0x10102030 What if you want to translate same virtual address again?



Kernel **CPU** Physical Memory Virtual Address Physical Address 0x104130 0x10102030 What if you want to translate same virtual address again?

Kernel **CPU** Virtual Address Physical Address 0x104130 0x10102030 What if you want to translate same virtual address again?

Physical Memory

Cache!!

Kernel **CPU** Virtual Address Physical Address 0x104130 0x10102030 What do you do with cache if there

Physical Memory



What do you do with cache if there is a context switch?

Kernel

CPU MMU



Kernel

CPU

MMU

Physical Memory

Virtual Address



Kernel

CPU

MMU

Physical Memory

Virtual Address 0x100



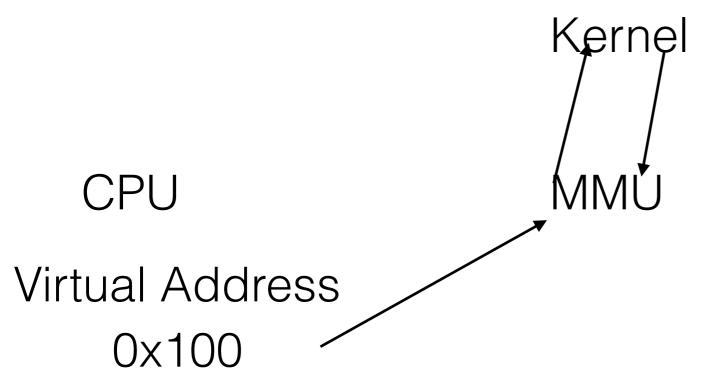
Kernel

CPU MMU
Virtual Address
0x100

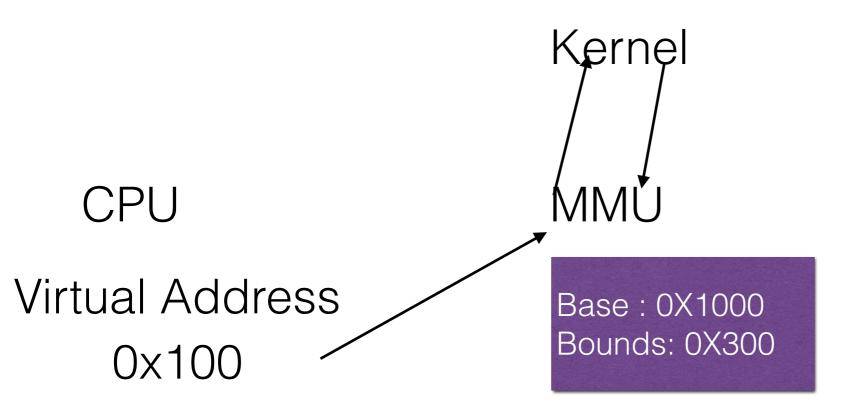


CPU MMU
Virtual Address
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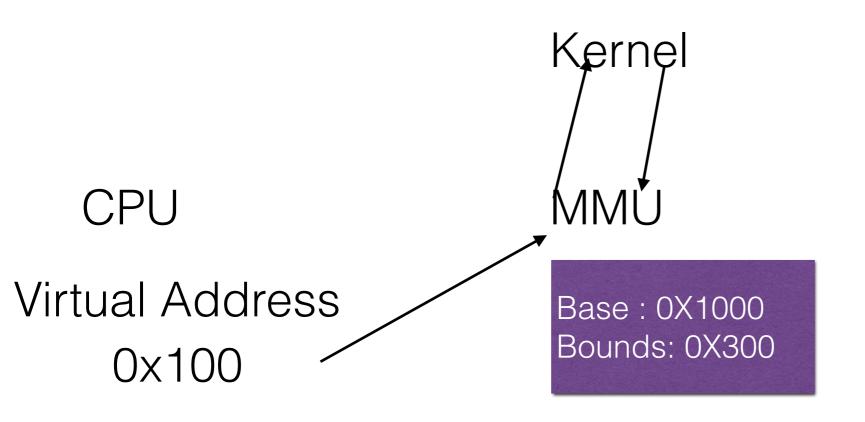


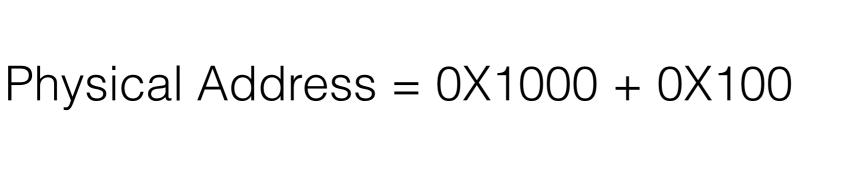




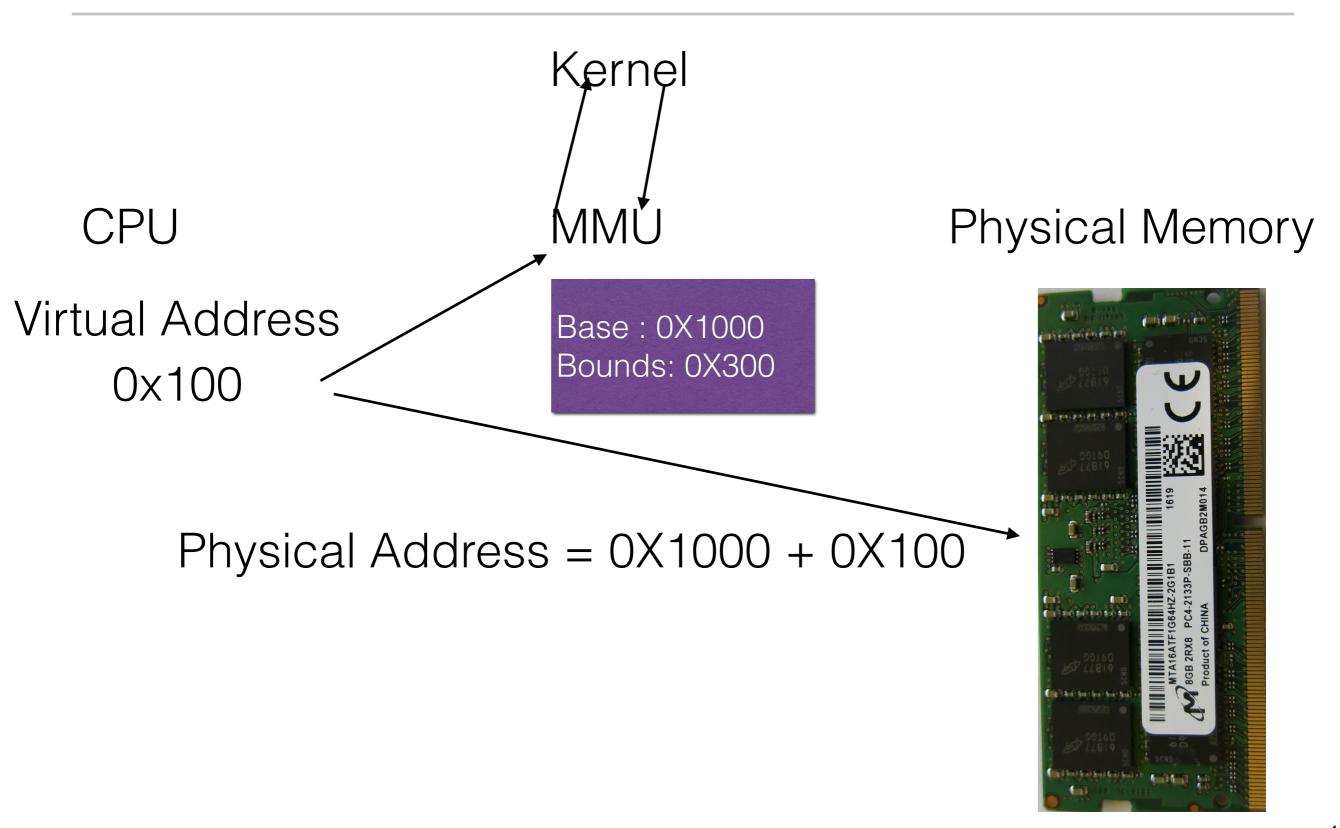


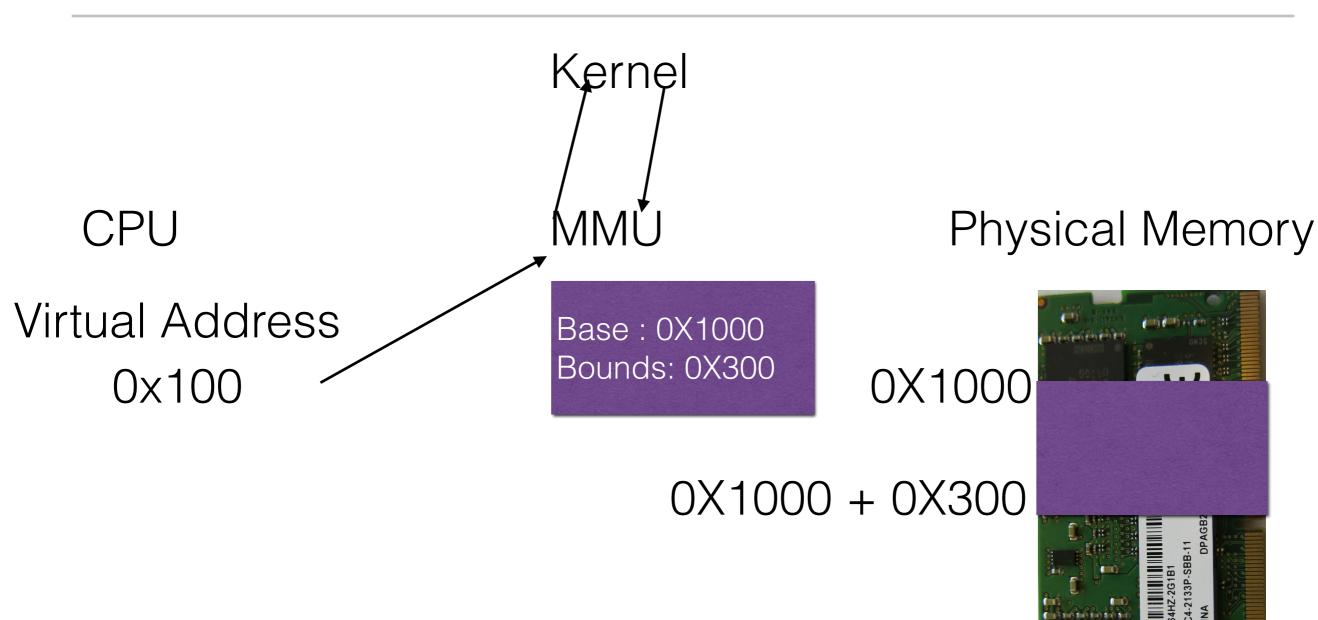


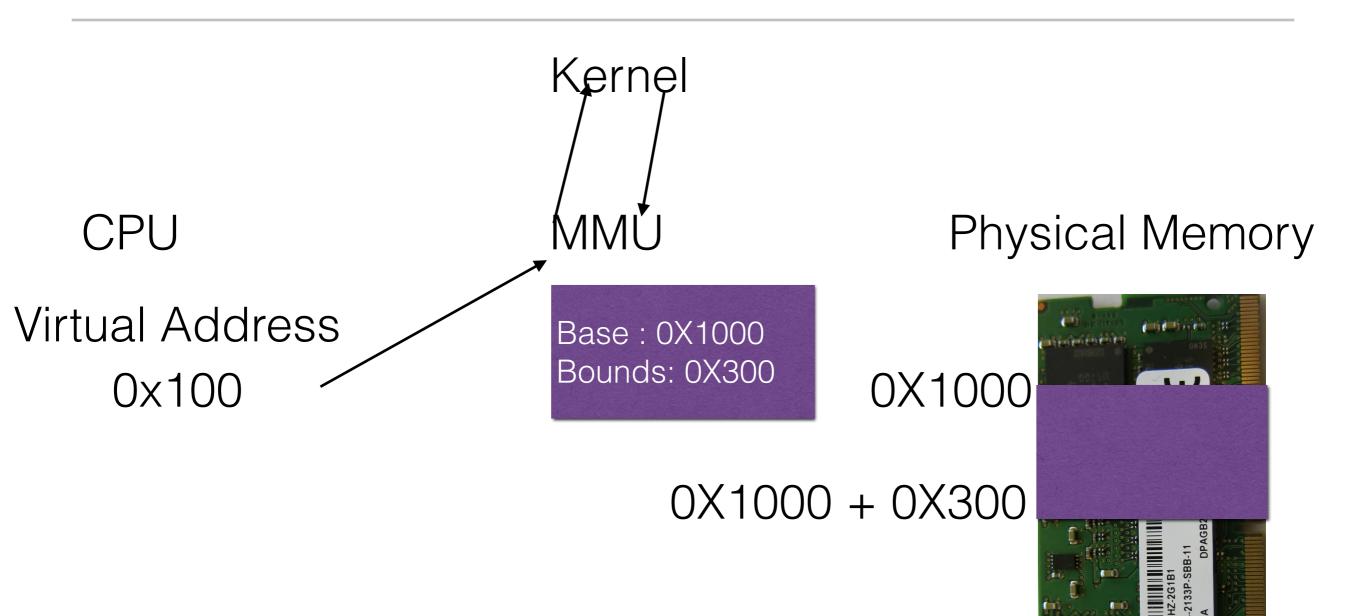




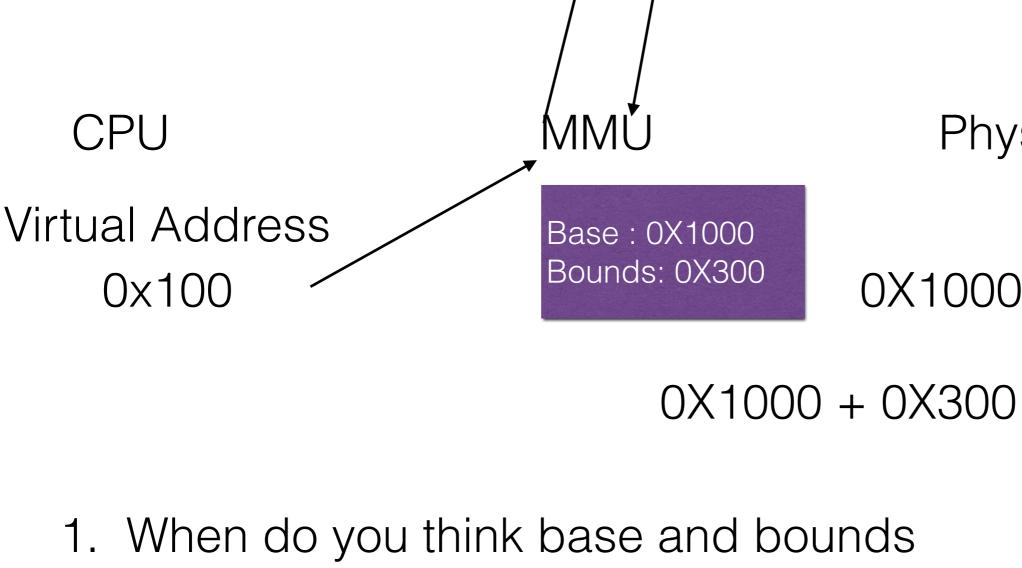






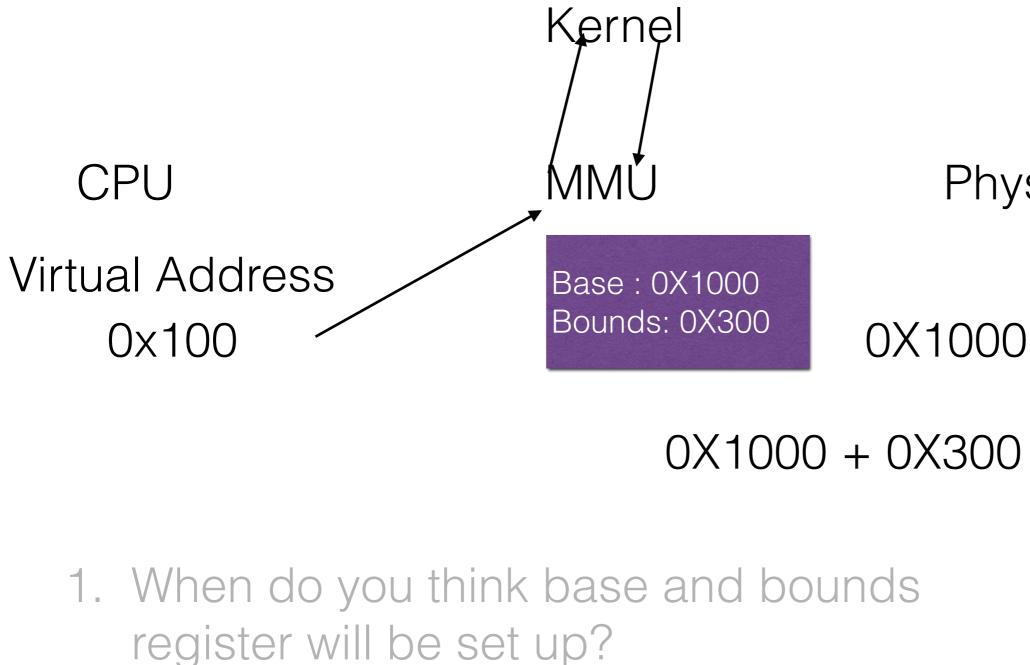


Kernel



register will be set up?



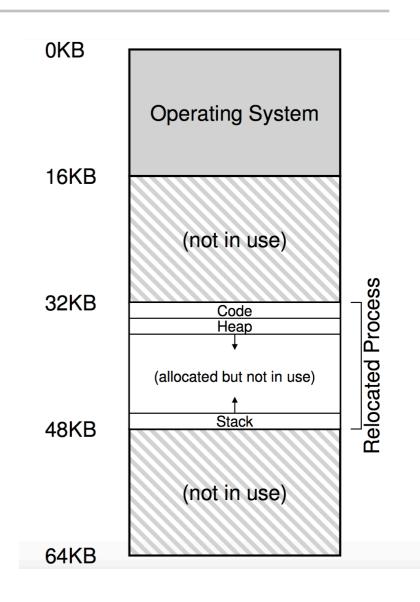


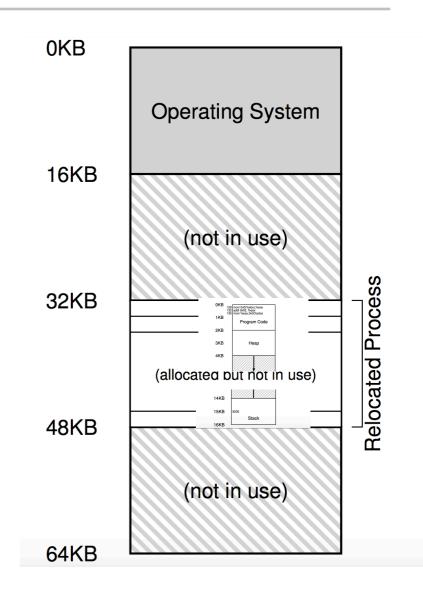
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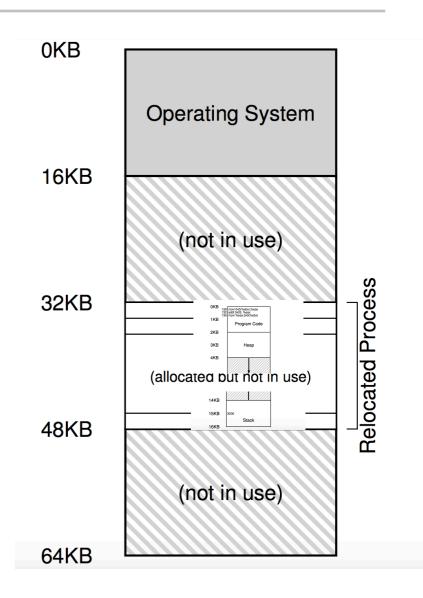
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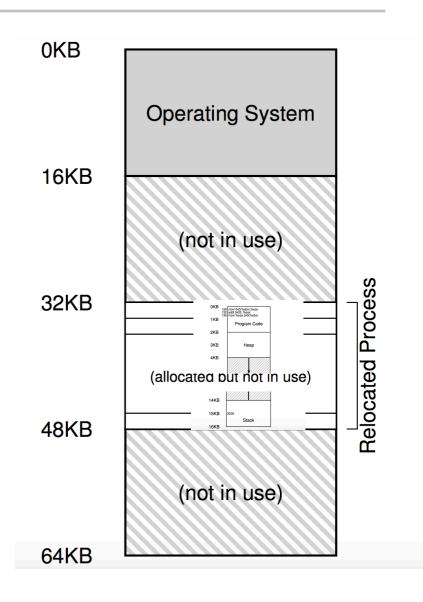
1. Base



### Dynamic (Hardware-based) Relocation

#### 1. Base

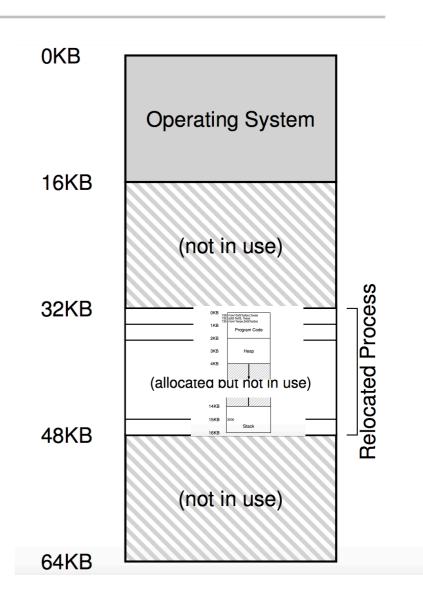
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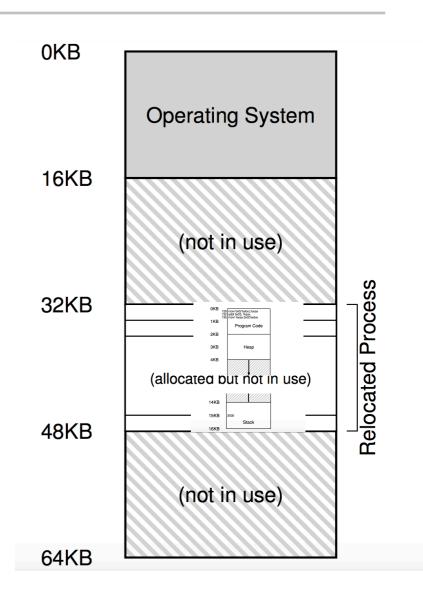
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### Dynamic (Hardware-based) Relocation

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- 3. Ans: 32 KB



128: movl 0x0(%ebx), %eax

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- 1. Transparency
  - 1. Virtual memory is invisible to user program
  - 2. Program thinks it has own private large memory
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  - 1. Not taking very long
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29

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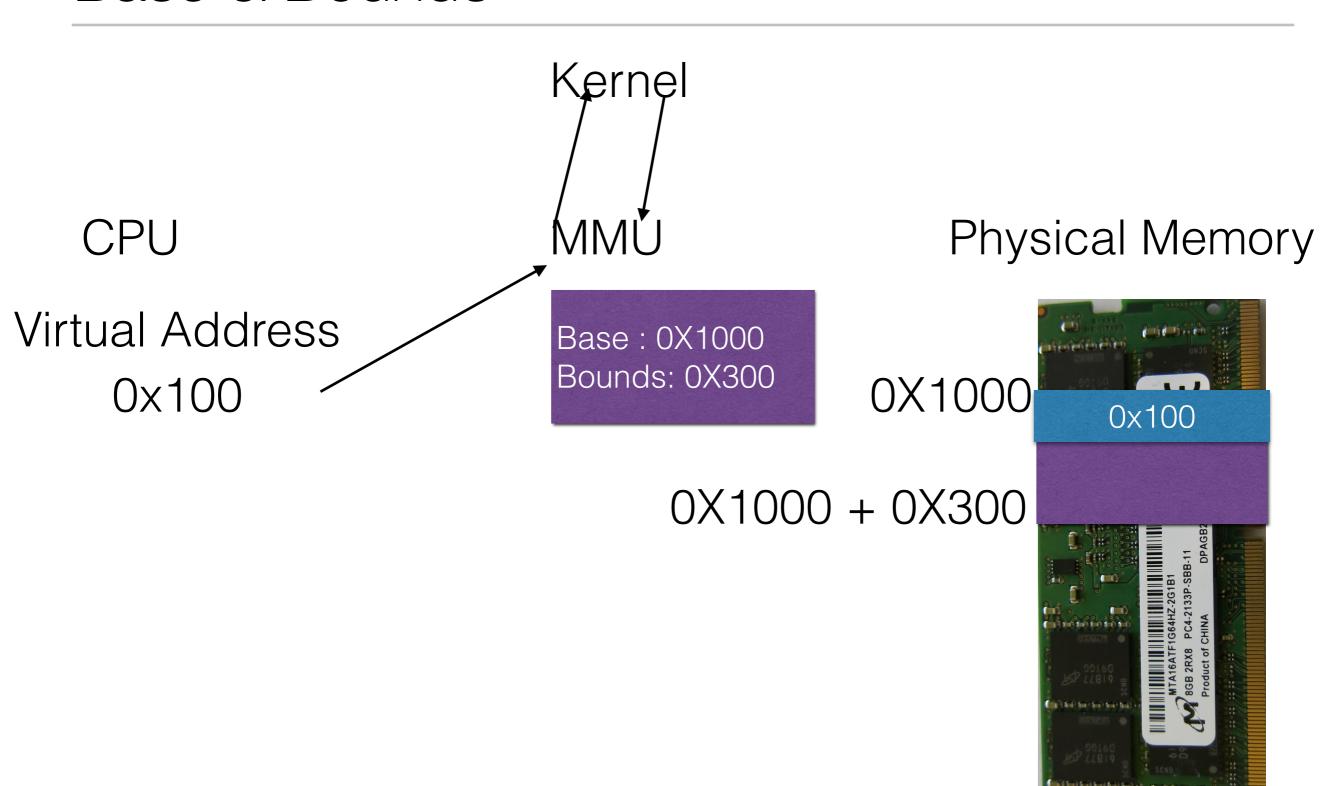
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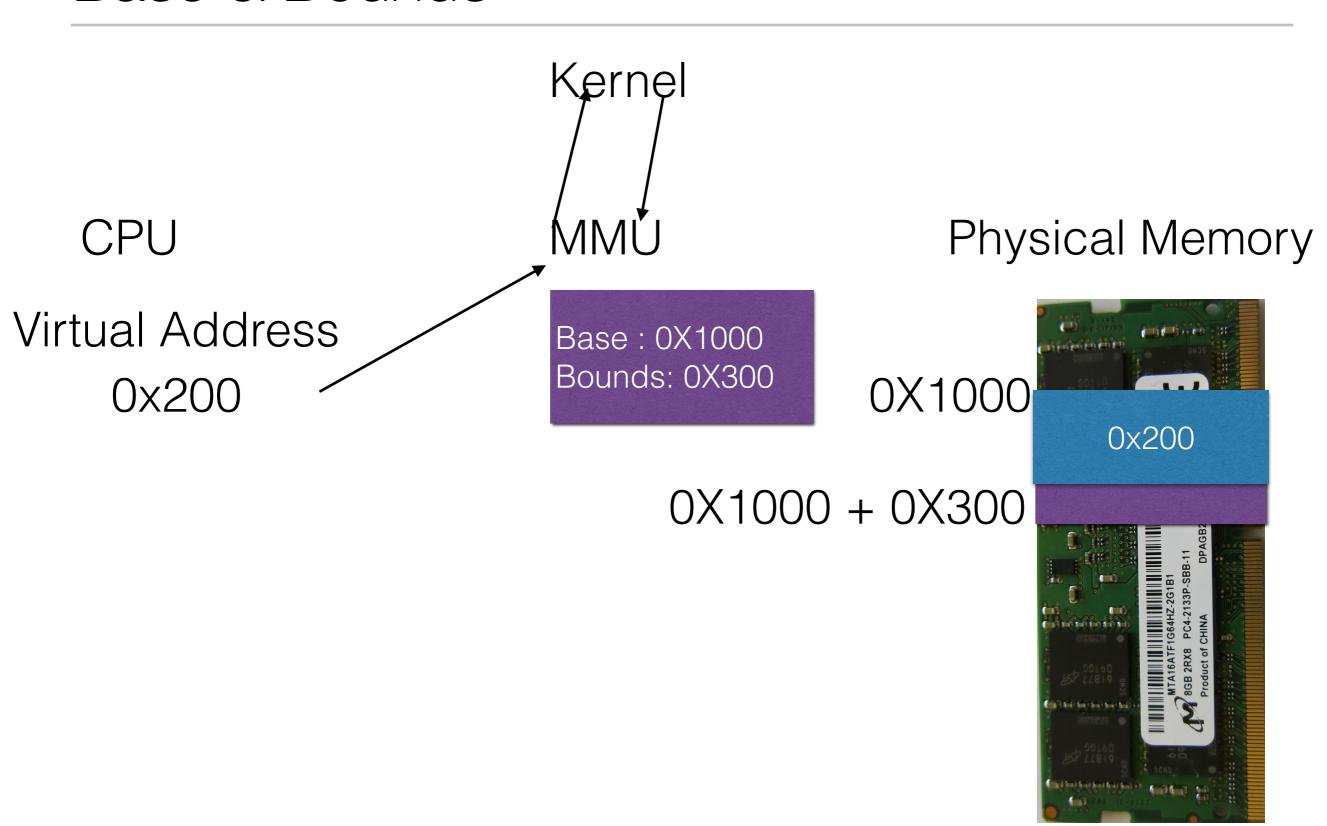
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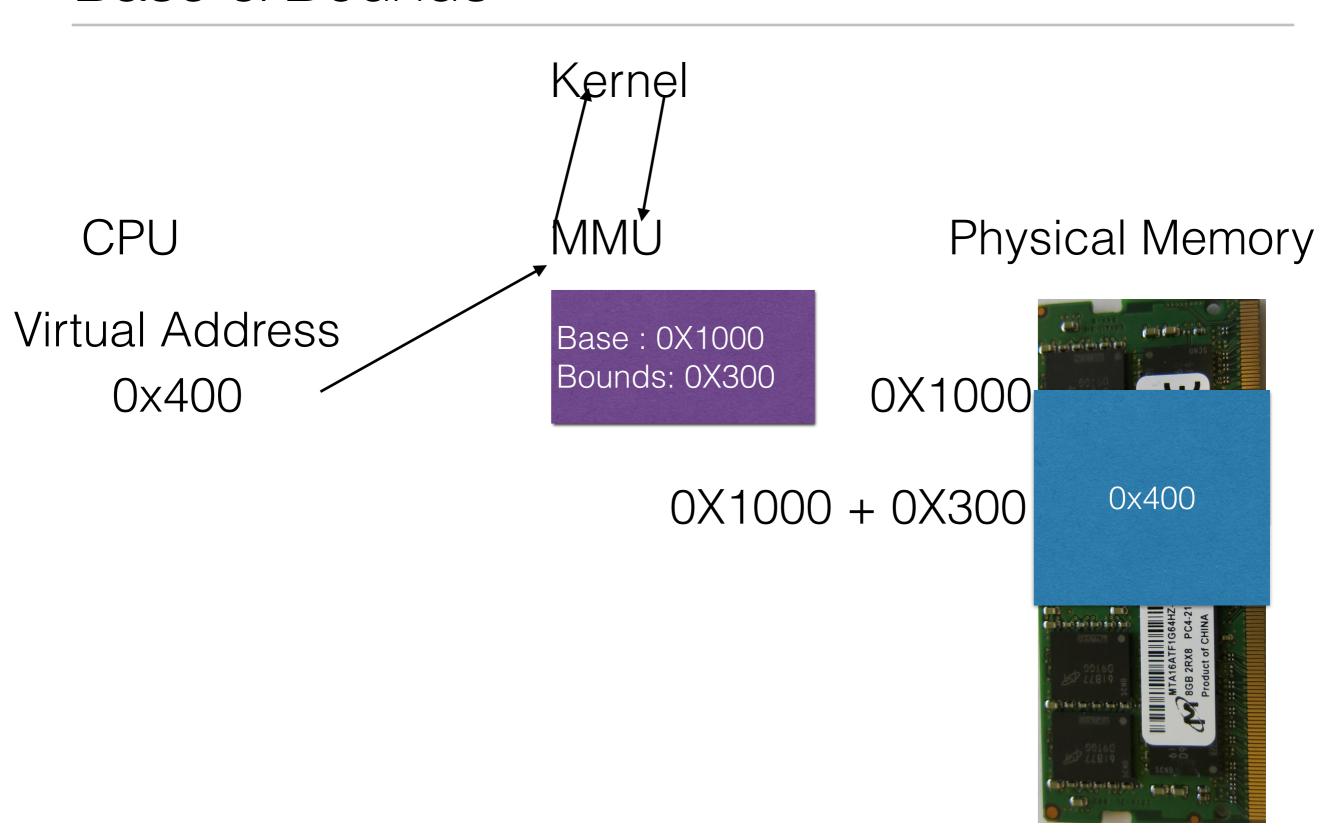
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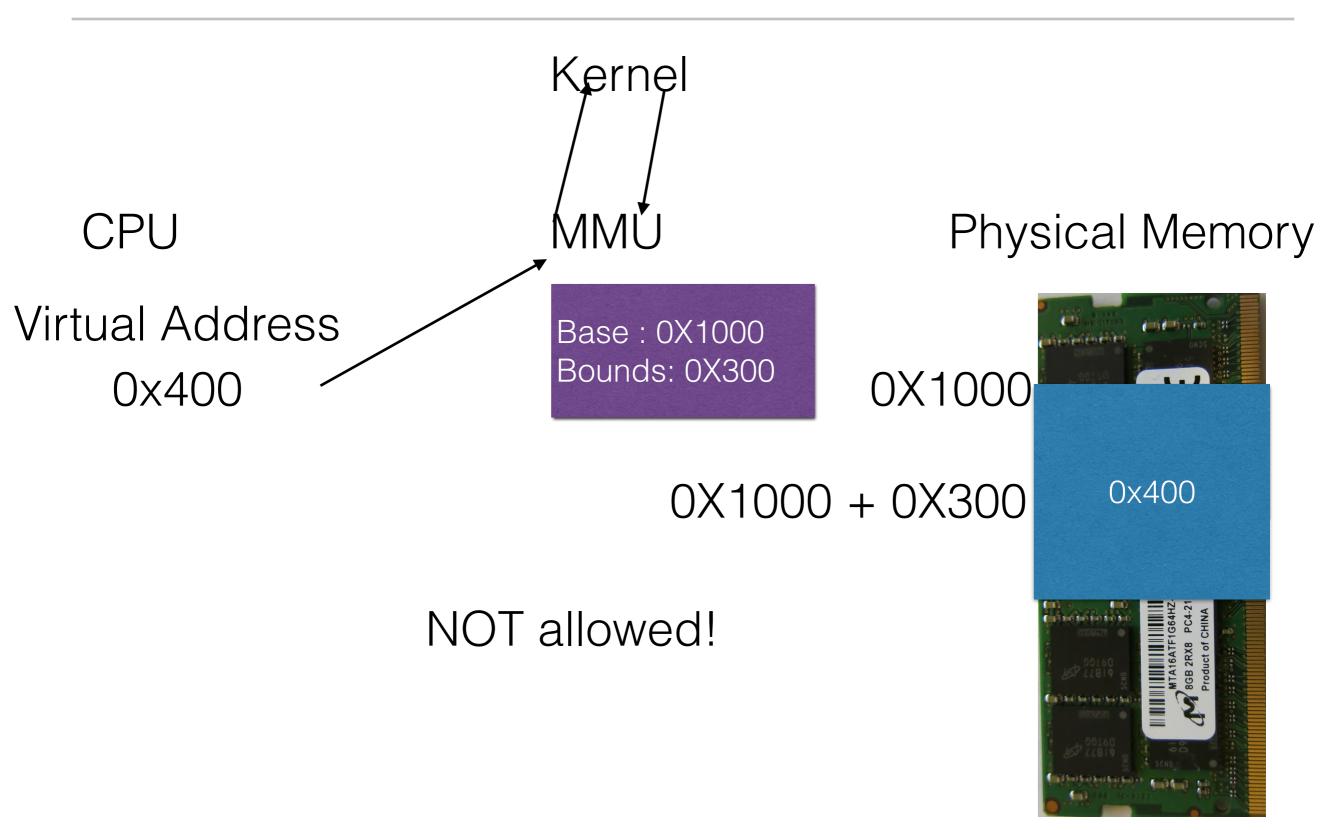
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Base/bounds registers	
Ability to translate virtual addresses	
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Privileged instruction(s) to	_
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exception handlers	
Ability to raise exceptions	_

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Ability to raise exceptions	When processes try to access privileged
	instructions or out-of-bounds memory

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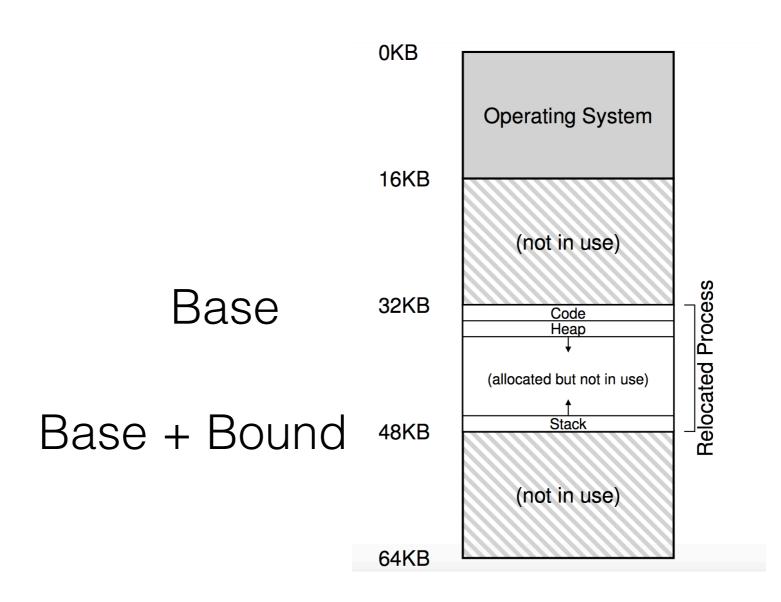
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1. Simple: Hardware only needs to know base & bounds

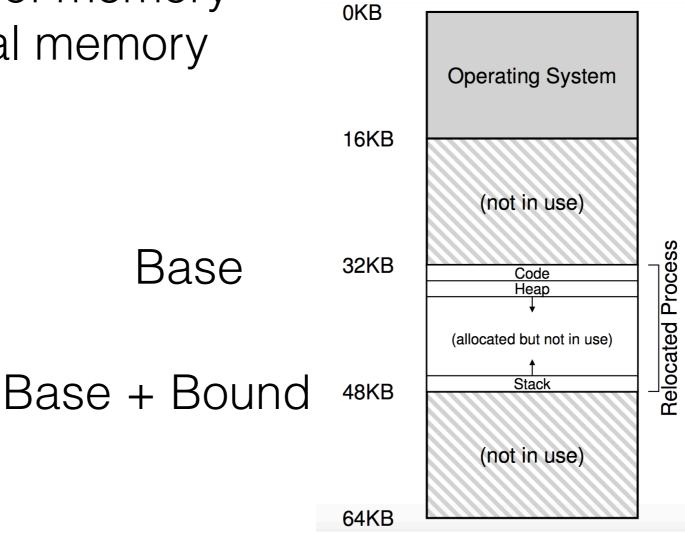
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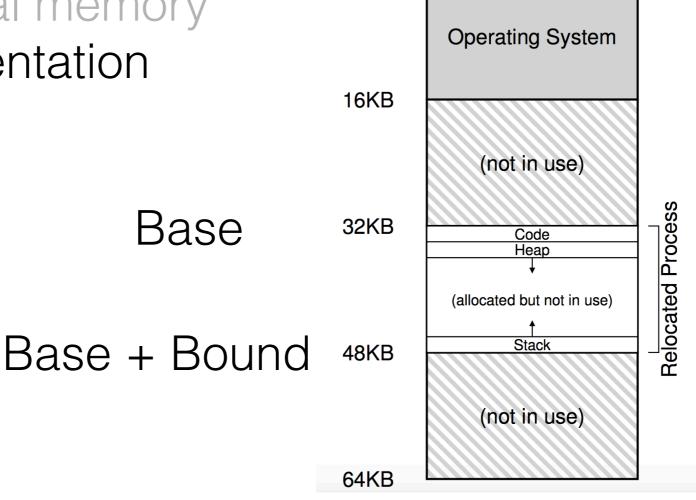
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  - 2. Translation: 1 addition



1. Contiguous block of memory needed in physical memory

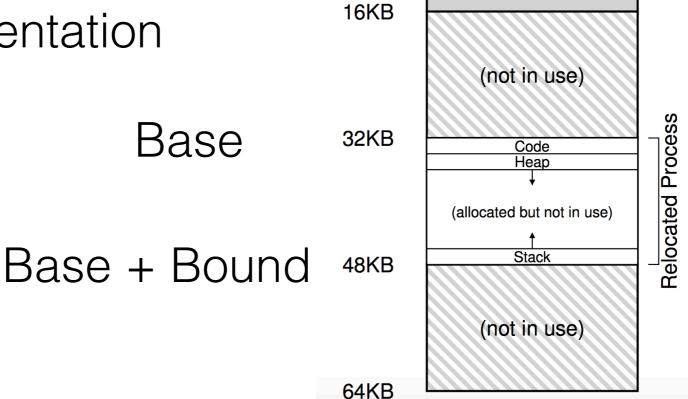


- 1. Contiguous block of memory needed in physical memory
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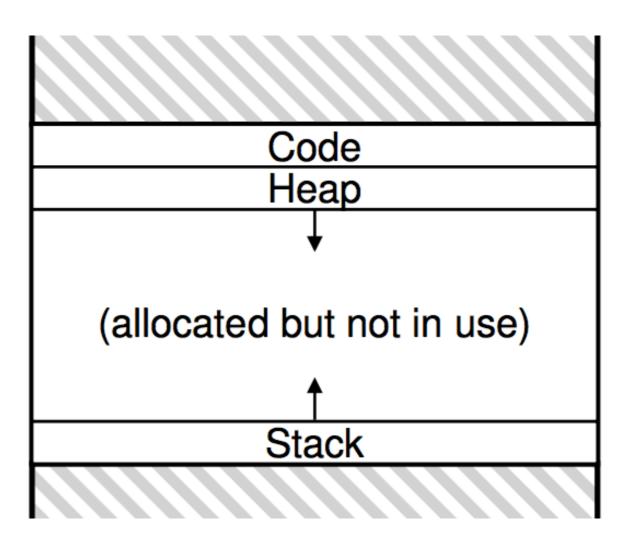
0KB

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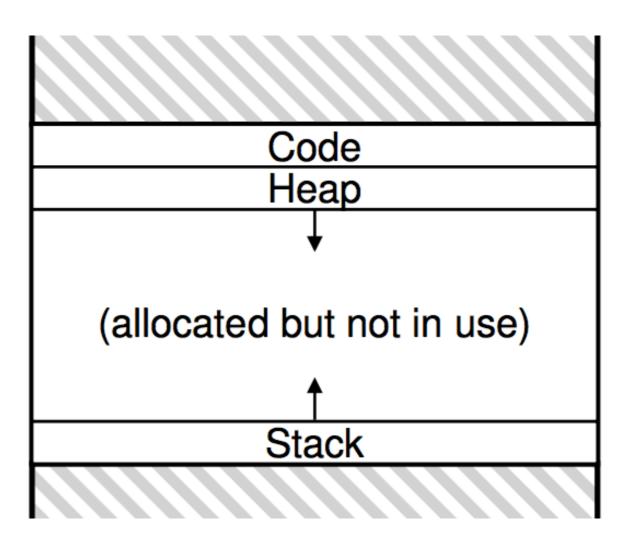


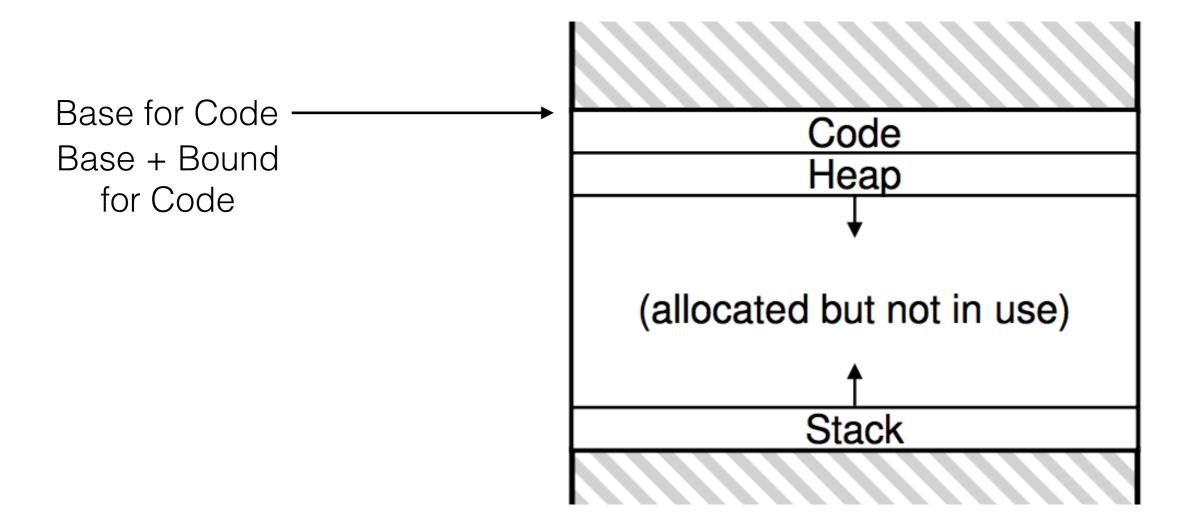
0KB

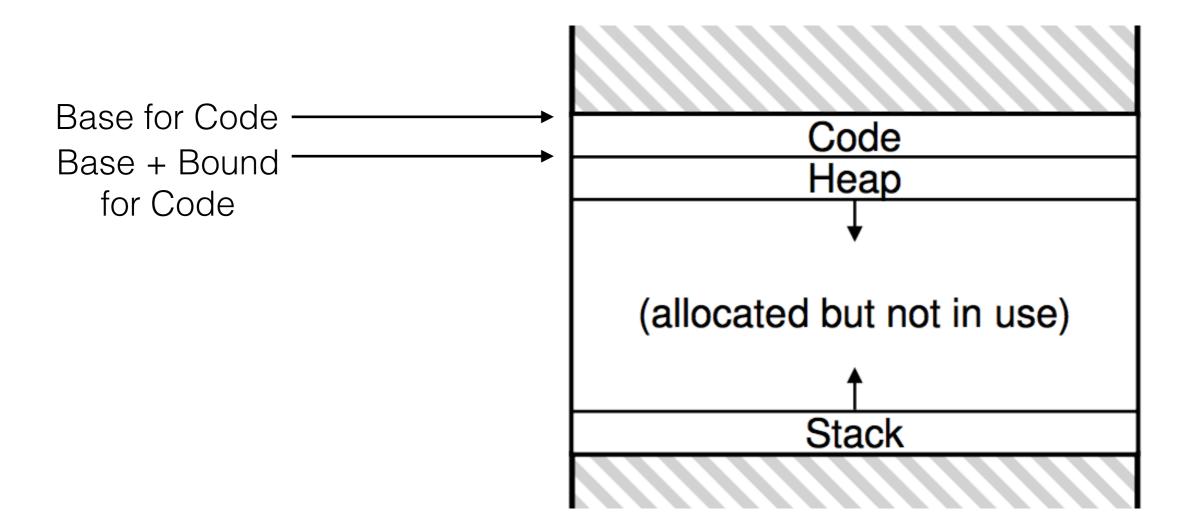
**Operating System** 

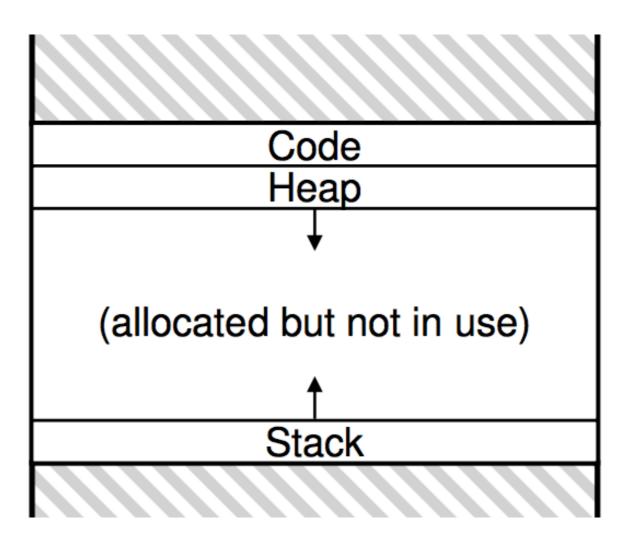


Base for Code Base + Bound for Code

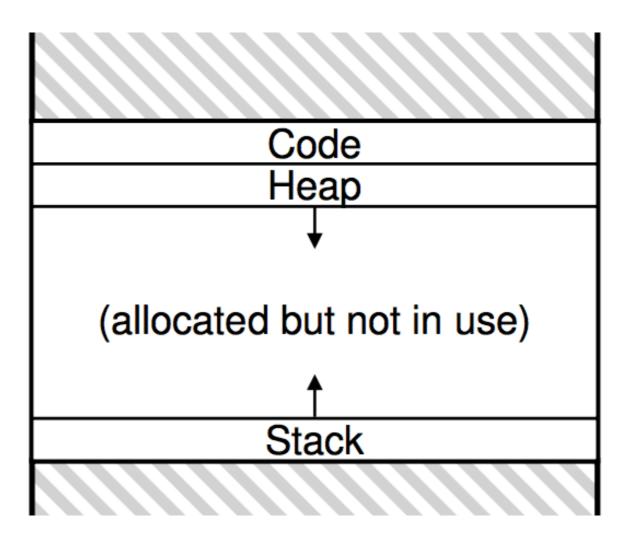


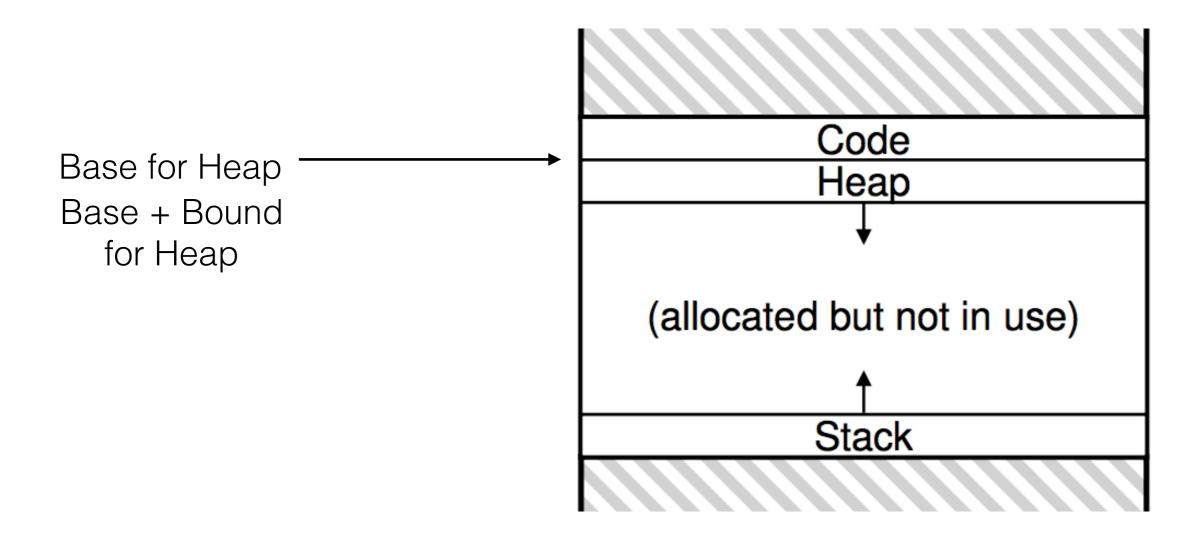


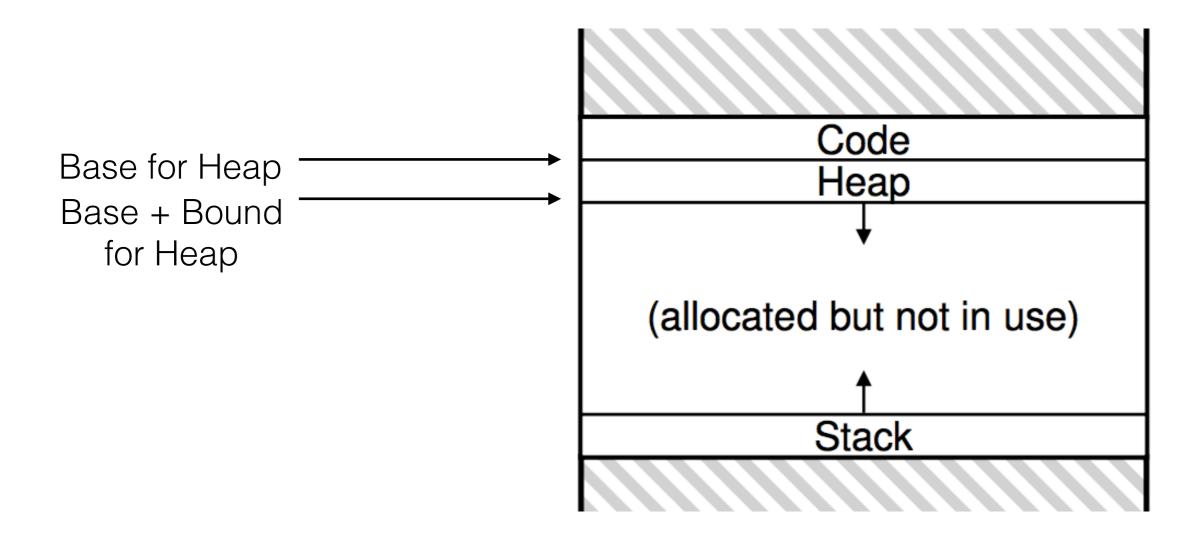


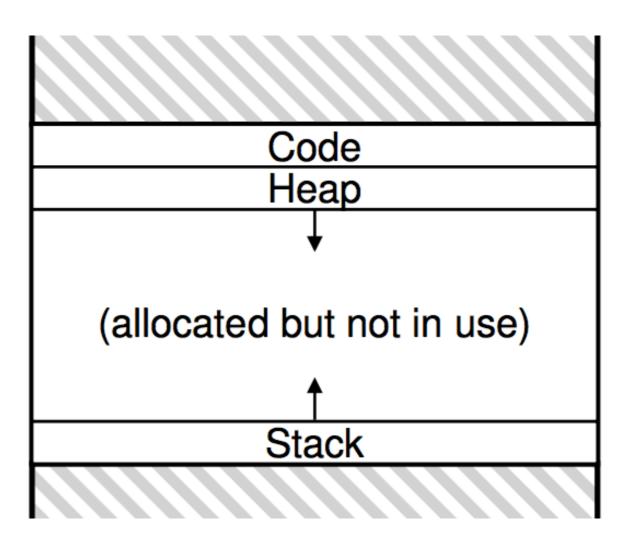


Base for Heap Base + Bound for Heap

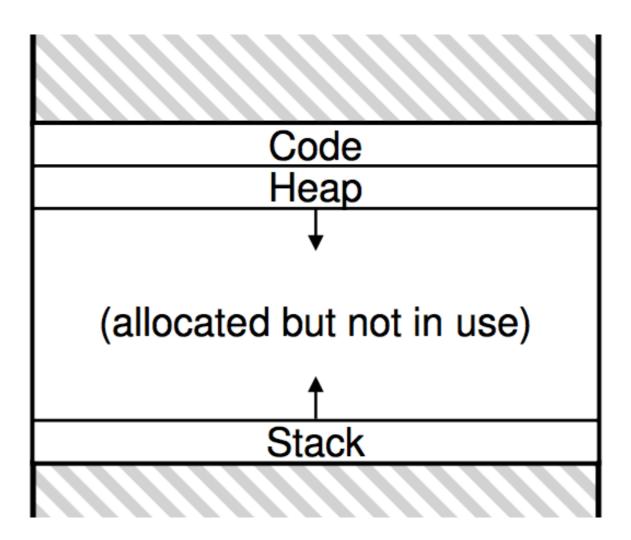


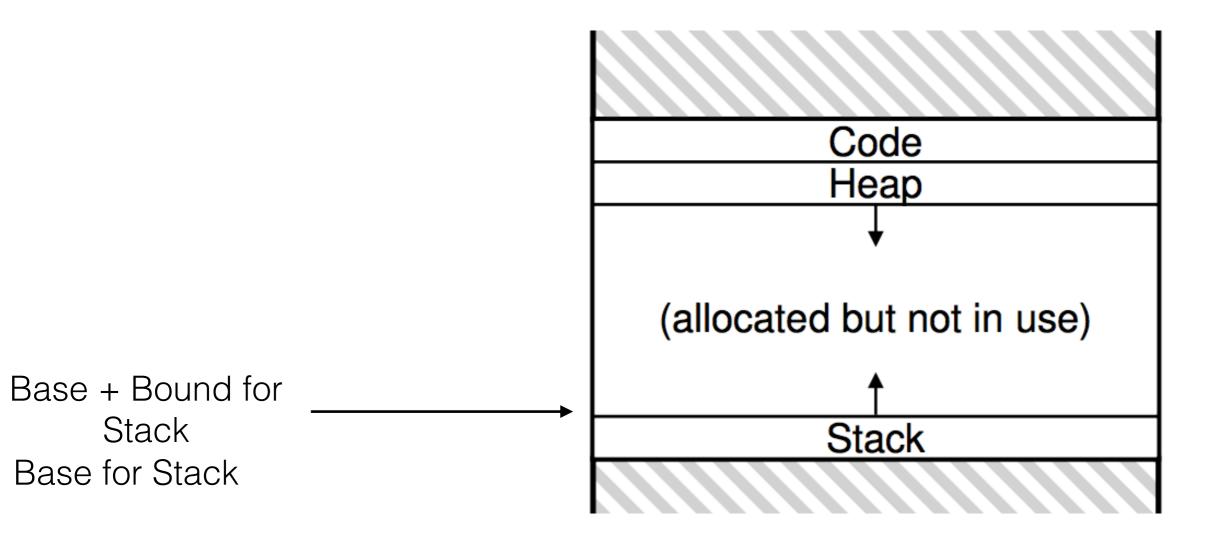


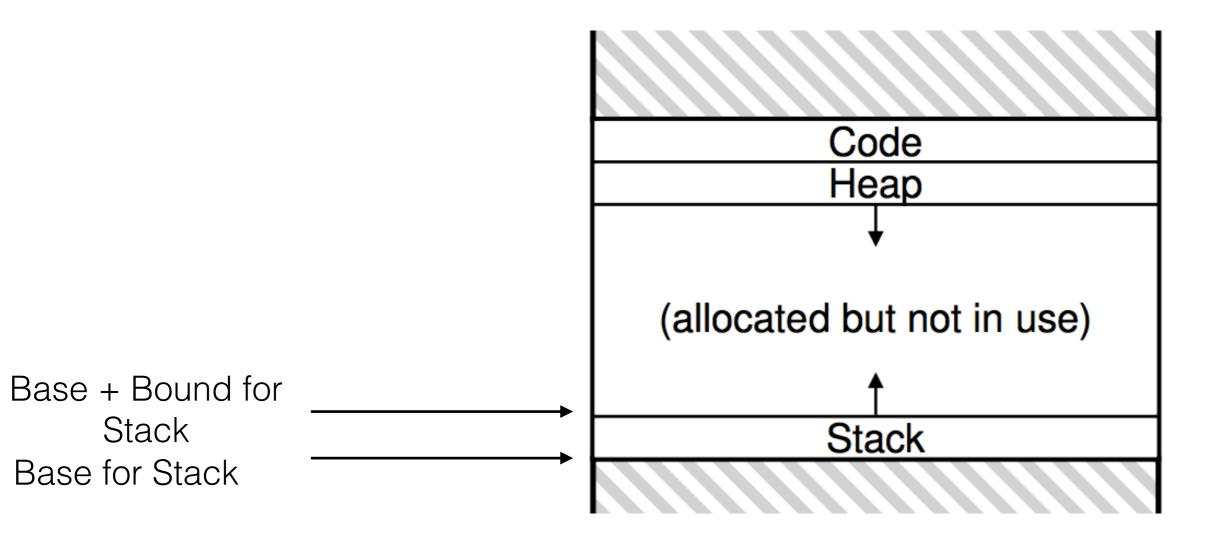




Base + Bound for Stack Base for Stack







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### Segmentation

- 1. Multiple bases and bounds per process. Each of them called a segment.
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  - 3. Each segment can have direction of growth!

1. Each segment has:

- 1. Each segment has:
  - 1. Start virtual address (VA)

- 1. Each segment has:
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  - 2. Base physical address

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- 3. **Translate**: For the segment that contains this virtual address:
  - 1. Physical Address = (V.A. Segment Start) + Segment Base

Kernel

CPU MMU

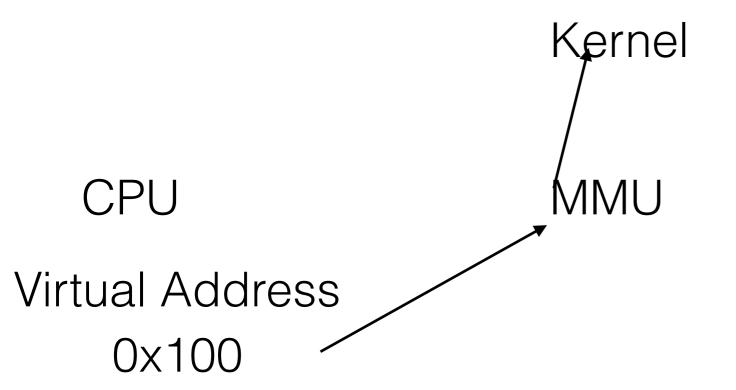
Virtual Address 0x100



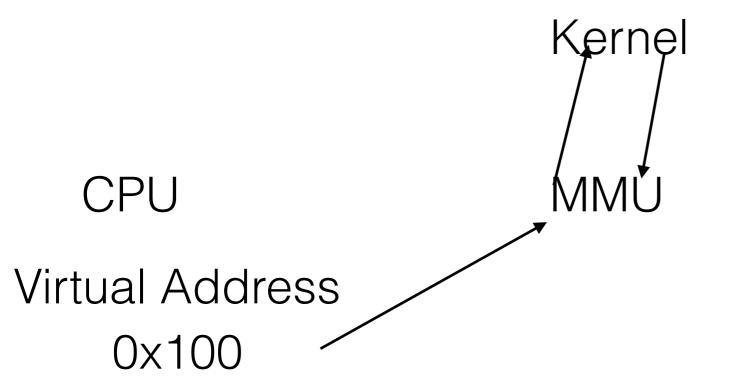
Kernel

CPU MMU
Virtual Address
0x100

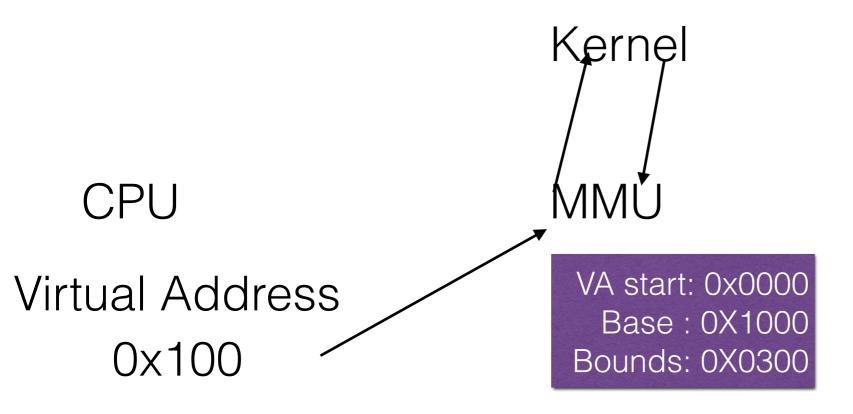




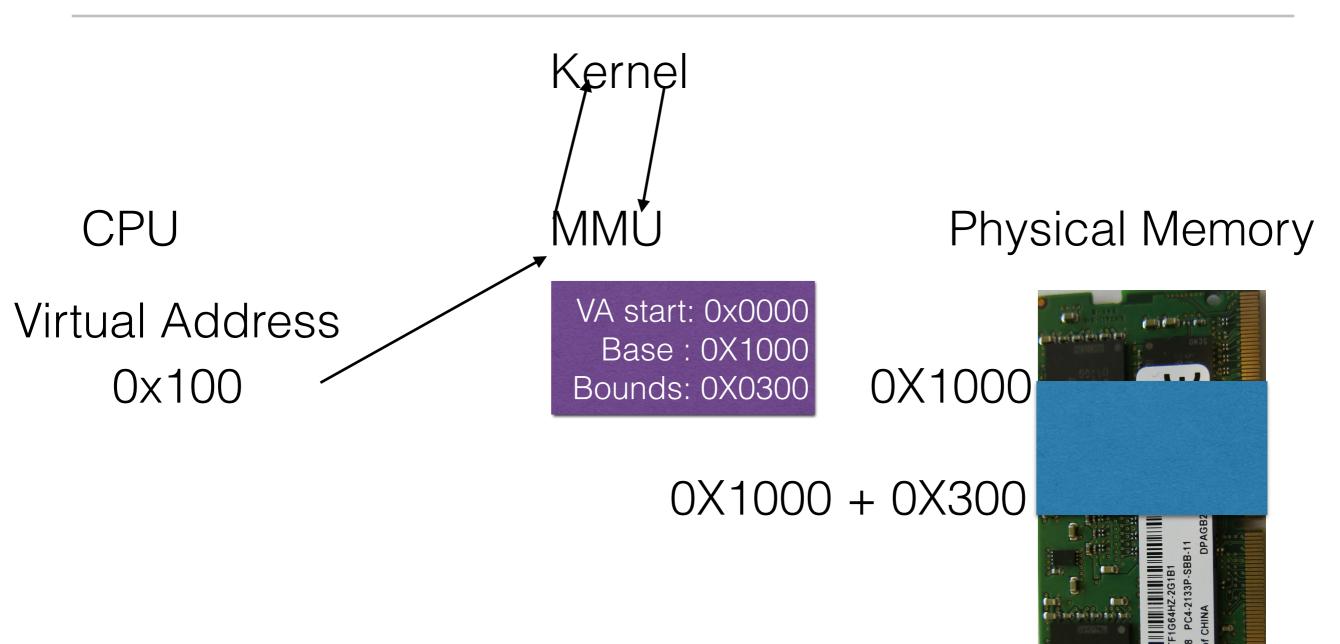


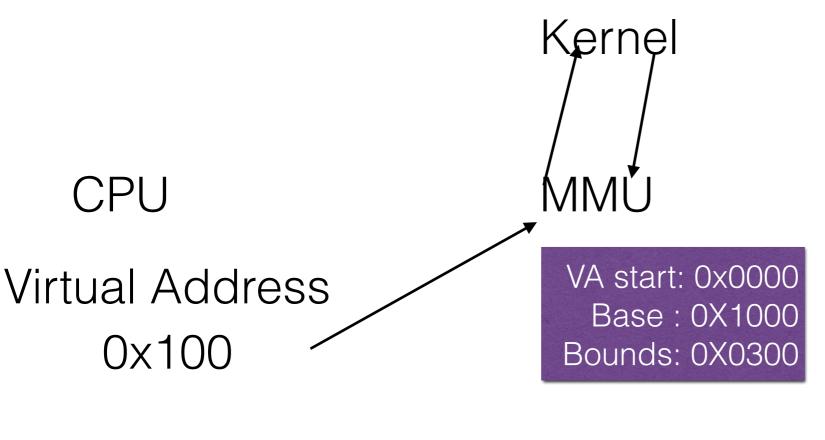












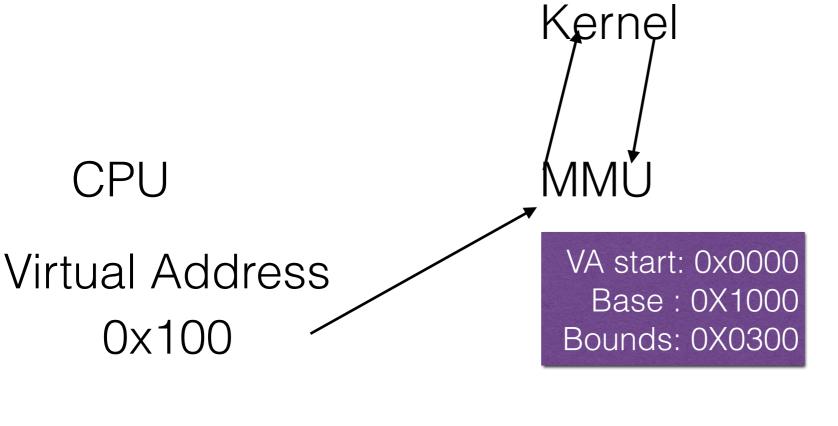
Physical Memory

0X1000

0X1000 + 0X300

Check: Segment Start < V.A. < Segment Start + Segment Bound.





Physical Memory

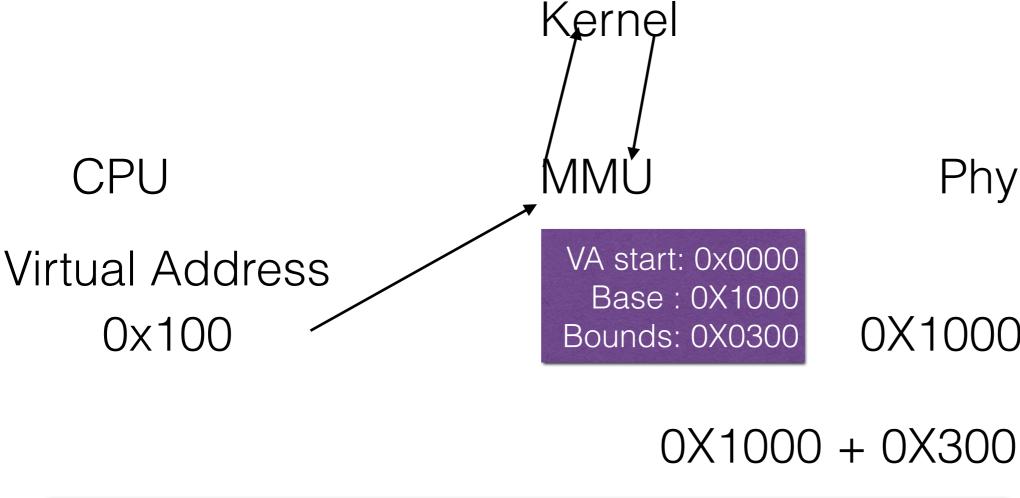
0X1000

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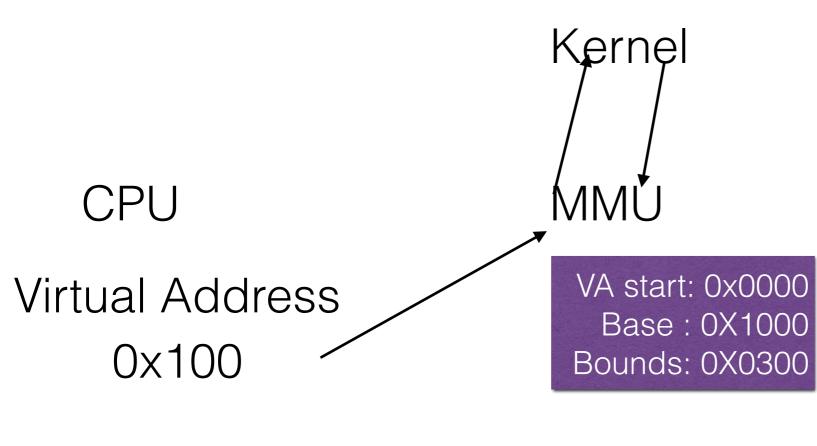
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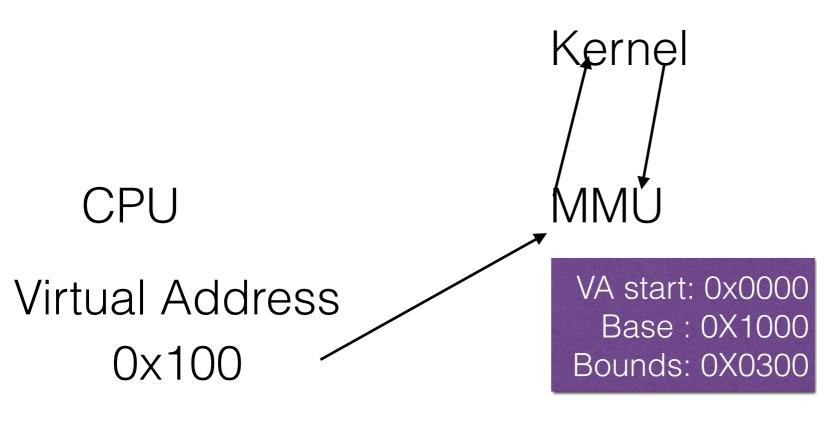
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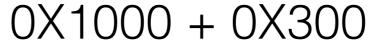
Physical Address = (V.A. - Segment Start) + Segment Base





Physical Memory

0X1000



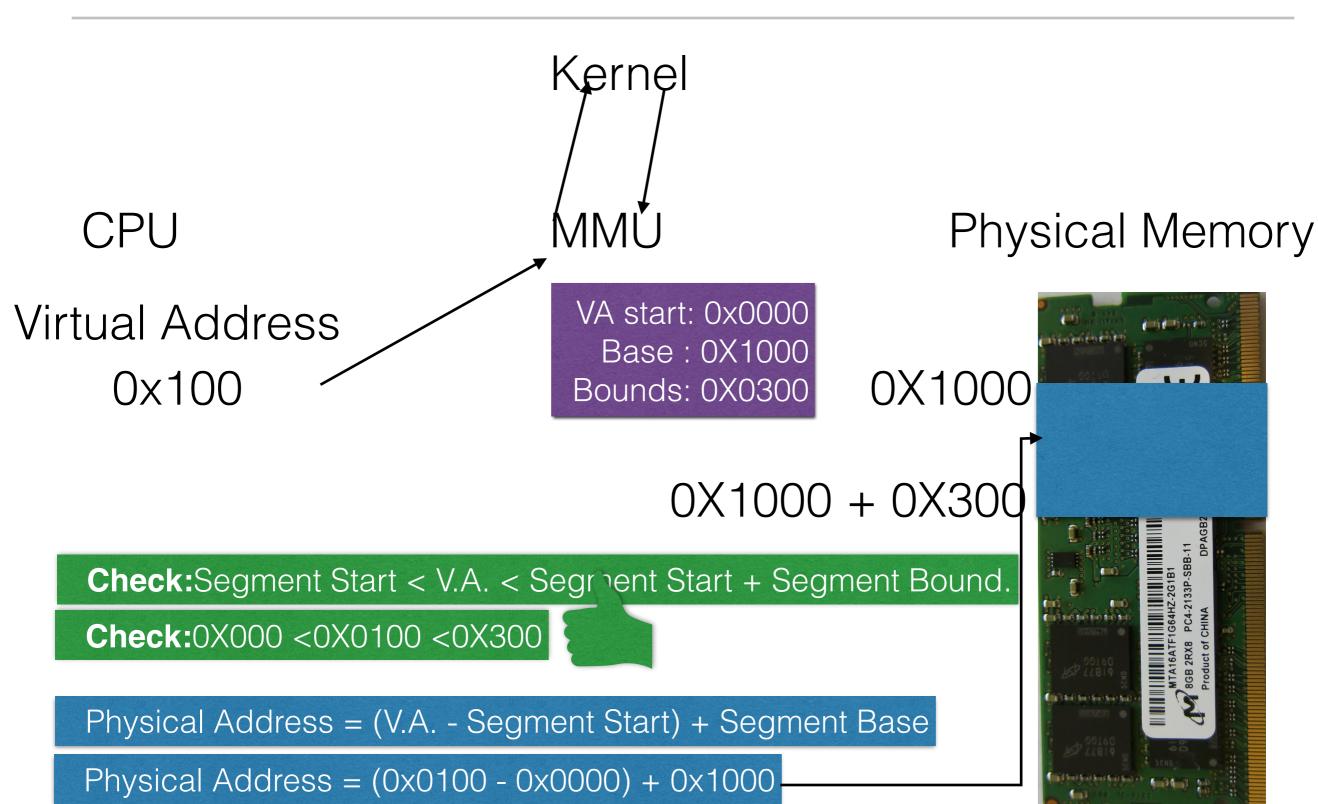
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Physical Address = (0x0100 - 0x0000) + 0x1000





Kernel

CPU MMU

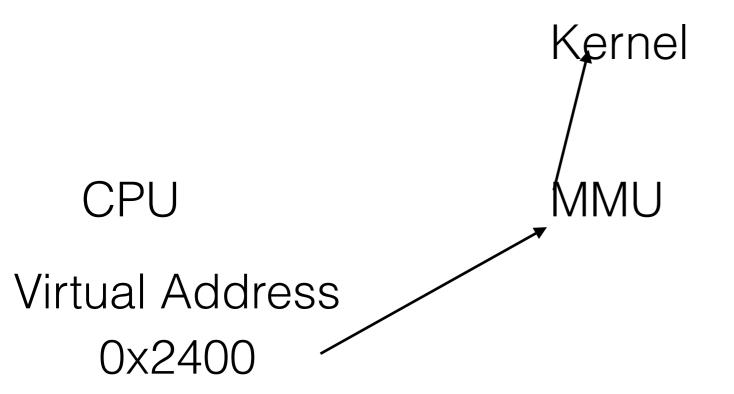
Virtual Address 0x2400



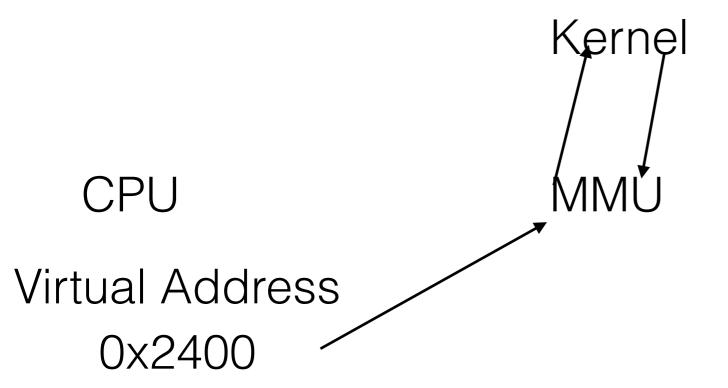
Kernel

CPU MMU
Virtual Address
0x2400

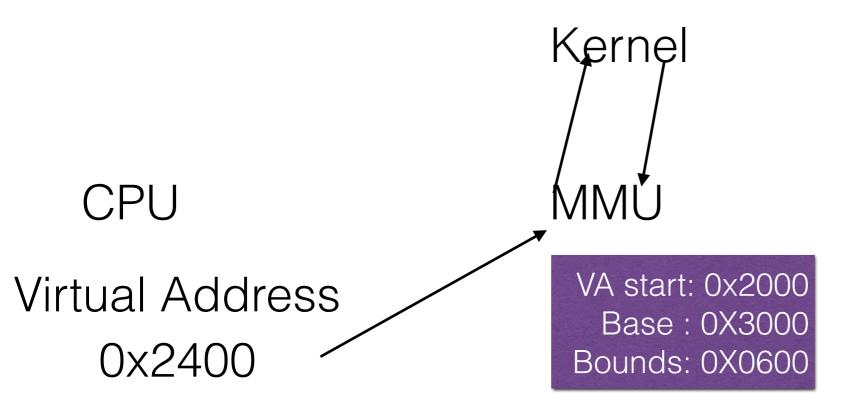




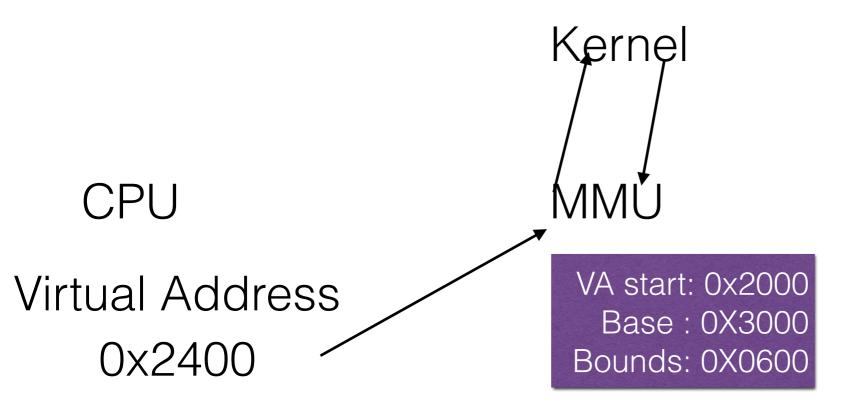




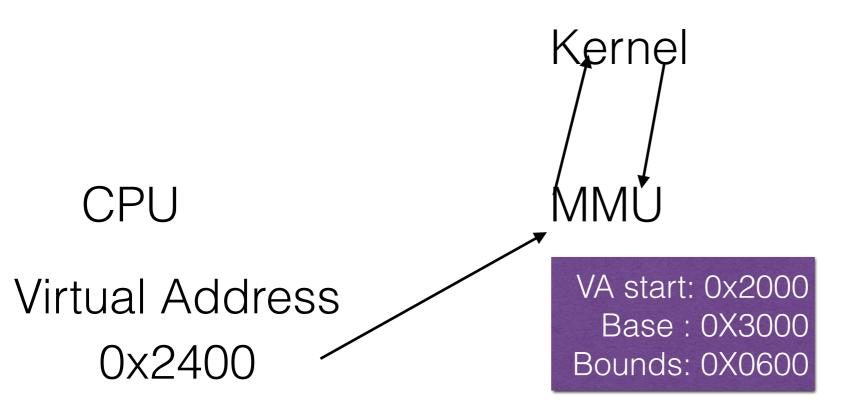








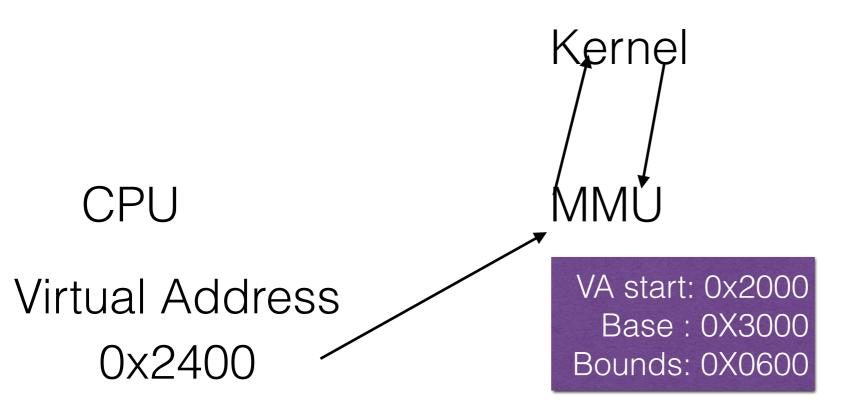




### Physical Memory



Check: Segment Start < V.A. < Segment Start + Segment Bound.



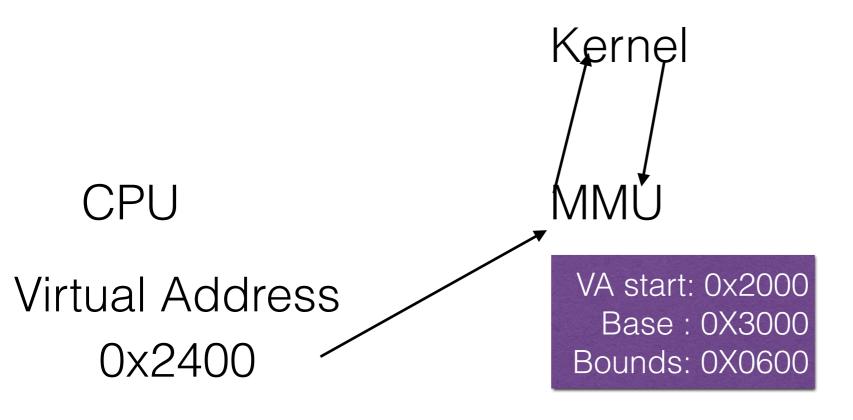
**Check:**0X2000 <0X2400 <0X2000+0X600

Check: Segment Start < V.A. < Segment Start + Segment Bound.

#### Physical Memory



0X3000

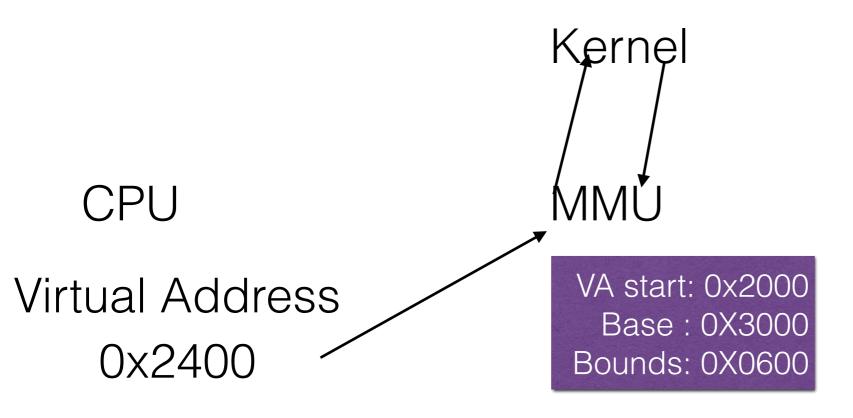


**Check:**0X2000 <0X2400 <0X2000+0X600

Check: Segment Start < V.A. < Segment Start + Segment Bound.

### Physical Memory





#### Physical Memory



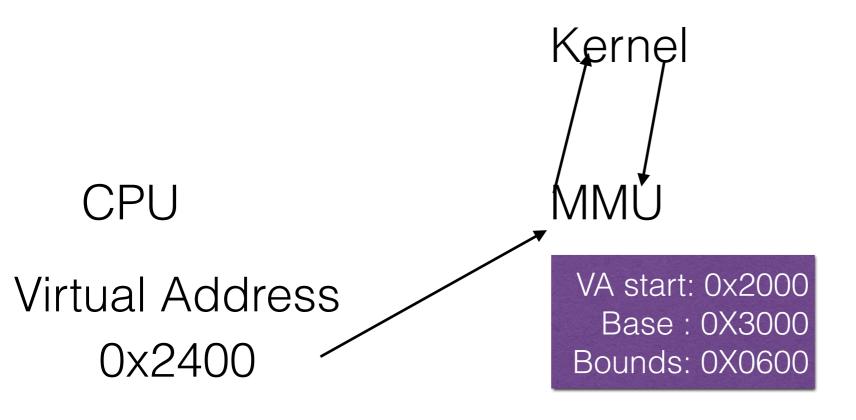
0X3000

Check: Segment Start < V.A. < Segment Start + Segment Bound.

**Check:**0X2000 <0X2400 <0X2000+0X600



Physical Address = (V.A. - Segment Start) + Segment Base



#### Physical Memory



0X3000

Check: Segment Start < V.A. < Segment Start + Segment Bound.

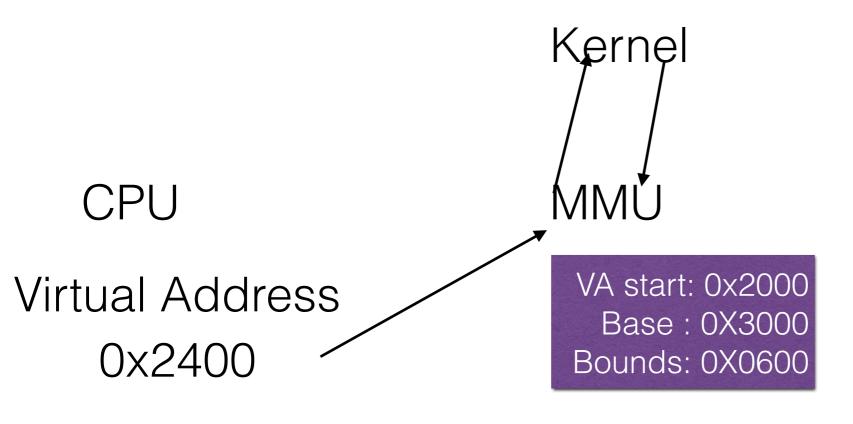
**Check:**0X2000 <0X2400 <0X2000+0X600



Physical Address = (V.A. - Segment Start) + Segment Base

Physical Address = (0x2400 - 0x2000) + 0x3000





### Physical Memory



0X3000

Check: Segment Start < V.A. < Segment Start + Segment Bound.

**Check:**0X2000 <0X2400 <0X2000+0X600



Physical Address = (0x2400 - 0x2000) + 0x3000

Kernel

CPU

MMU

Physical Memory

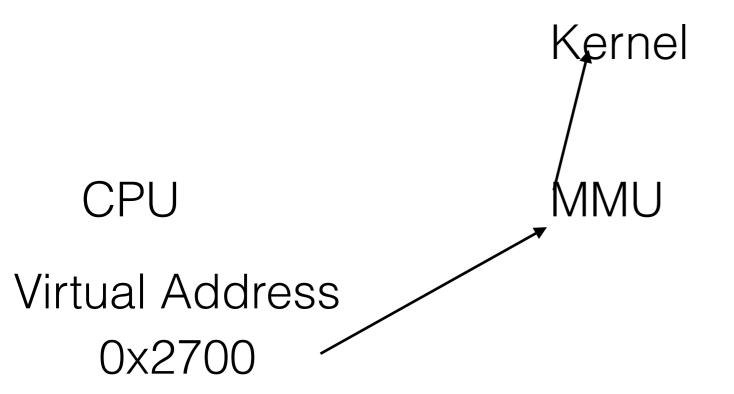
Virtual Address 0x2700



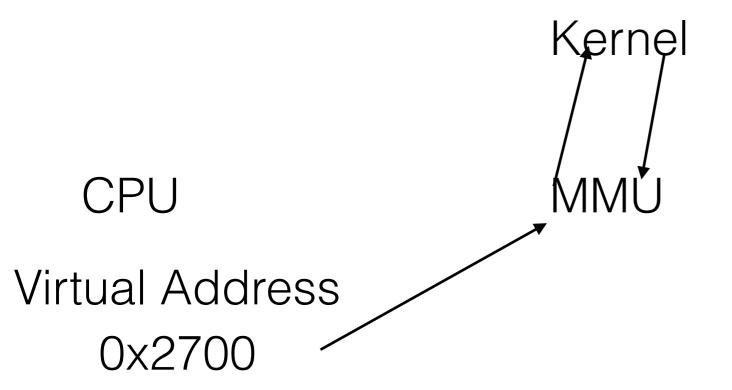
Kernel

CPU MMU
Virtual Address
0x2700

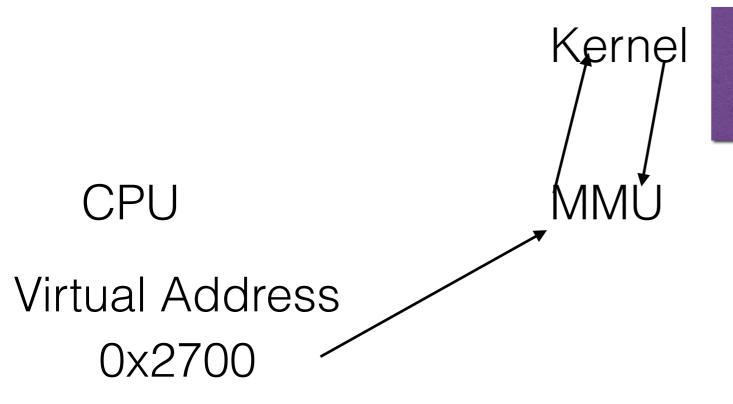






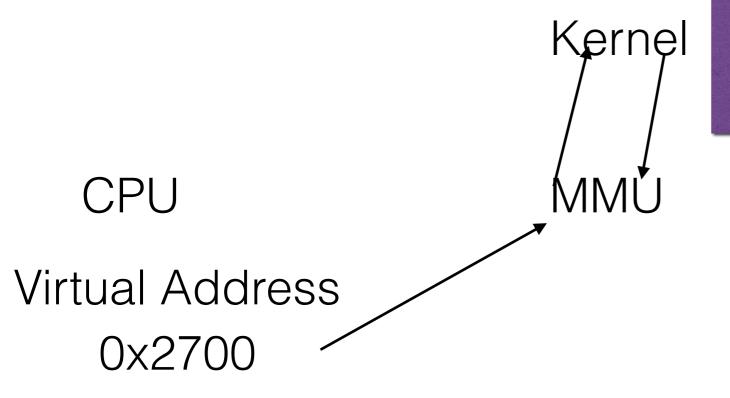






VA start: 0x0000 Base : 0X1000 Bounds: 0X0300





VA start: 0x0000 Base : 0X1000

Bounds: 0X0300

VA start: 0x2000 Base : 0X3000

Bounds: 0X0600



CPU MMU
Virtual Address
0x2700

**Check:**Segment Start < V.A. < Segment Start + Segment Bound.

VA start: 0x0000

Base: 0X1000

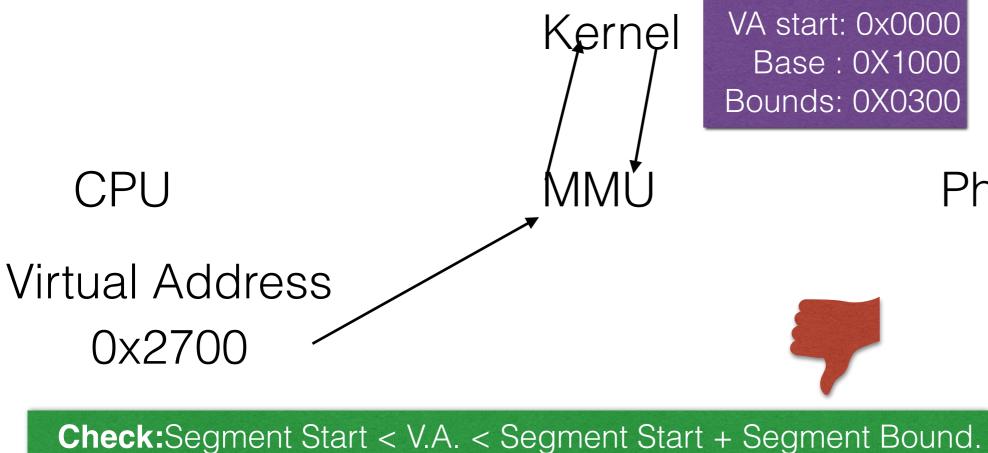
Bounds: 0X0300

VA start: 0x2000

Base: 0X3000

Bounds: 0X0600





Base : 0X1000 Base : 0X3000 bunds: 0X0300 Bounds: 0X0600



CPU MMU
Virtual Address
0x2700

VA start: 0x0000

Base: 0X1000

Bounds: 0X0300

VA start: 0x2000

Base: 0X3000

Bounds: 0X0600

Physical Memory



**Check:**Segment Start < V.A. < Segment Start + Segment Bound.

#### SEGMENTATION FAULT





